

FIG. 1a

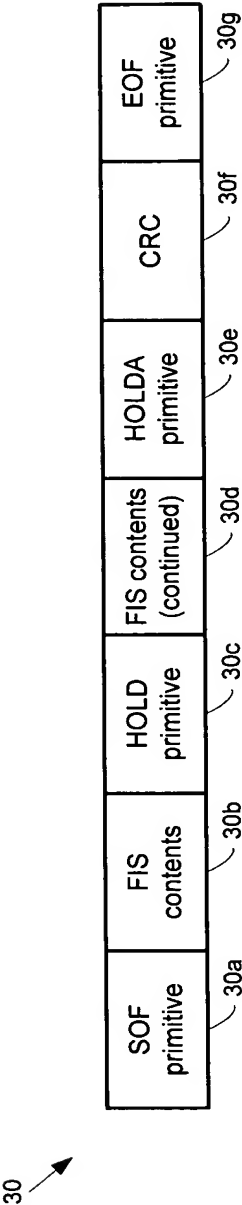


FIG. 1b

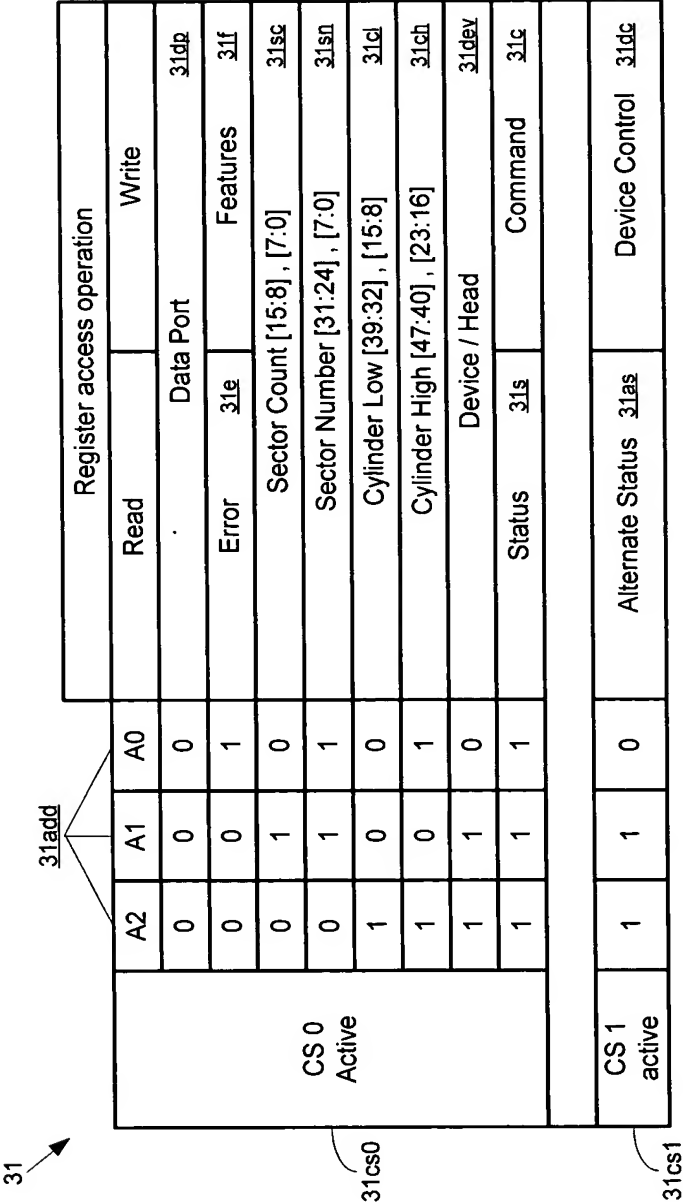


FIG. 1c

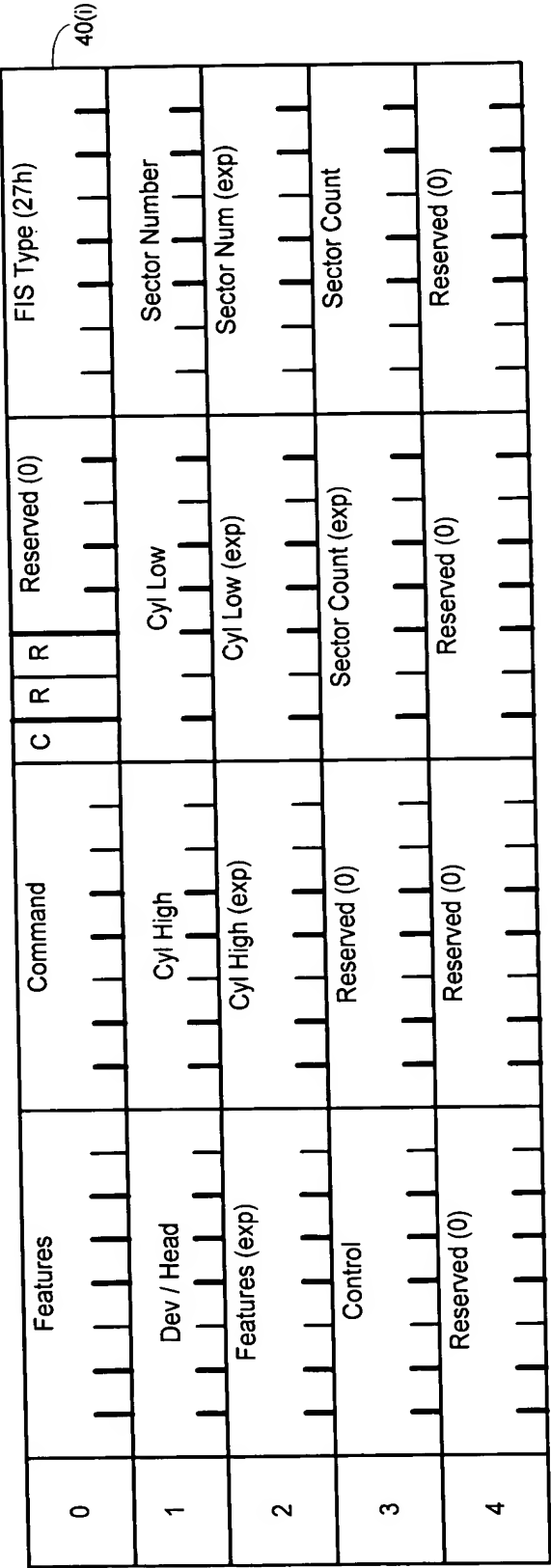


FIG. 1d(i) Register FIS Host to Device

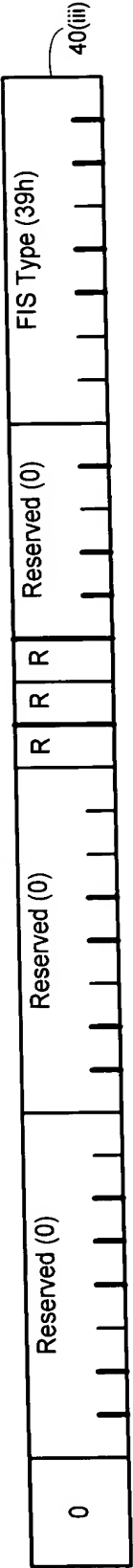


FIG. 1d(iii) DMA Activate FIS, Device to Host

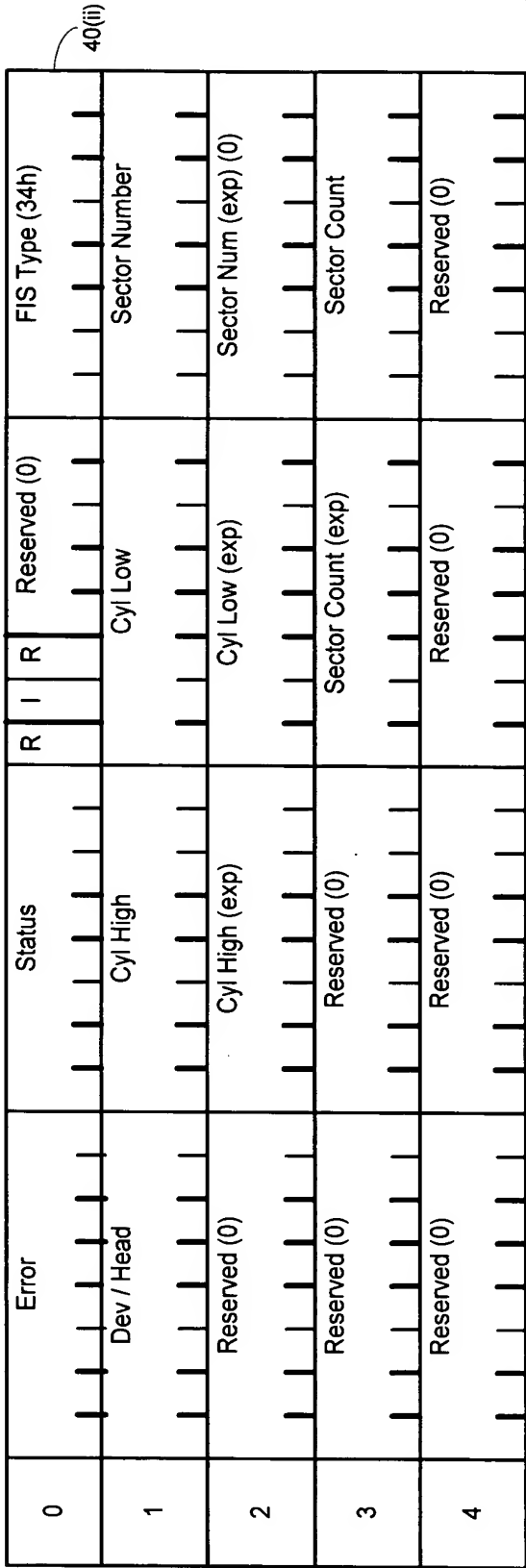


FIG. 1d(ii) Register FIS Device to Host

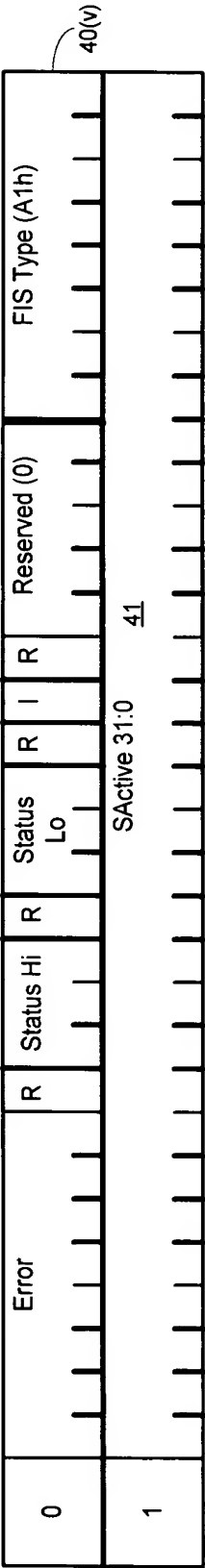


FIG. 1d(v) Set Device Bits FIS

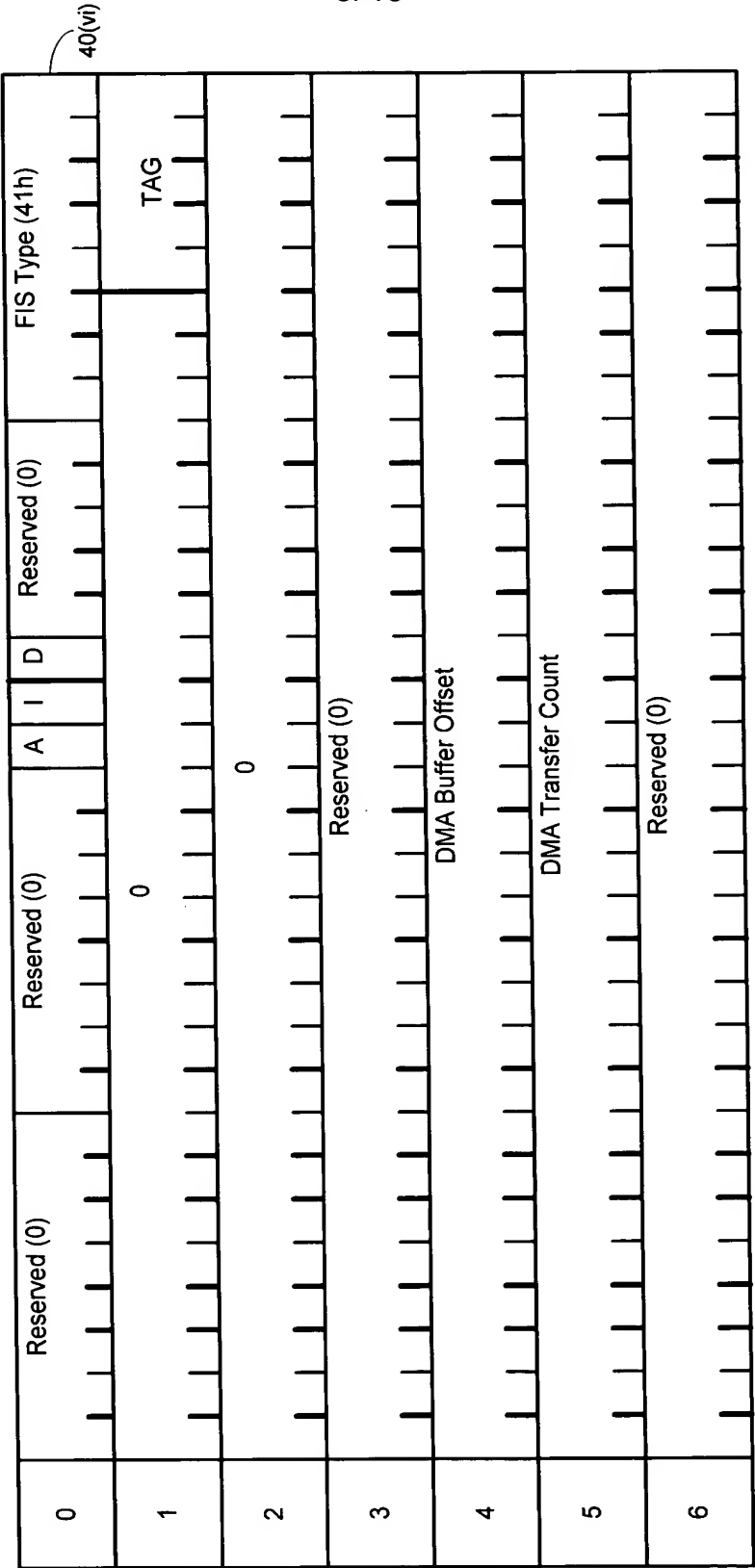


FIG. 1d(iv) DMA Setup FIS

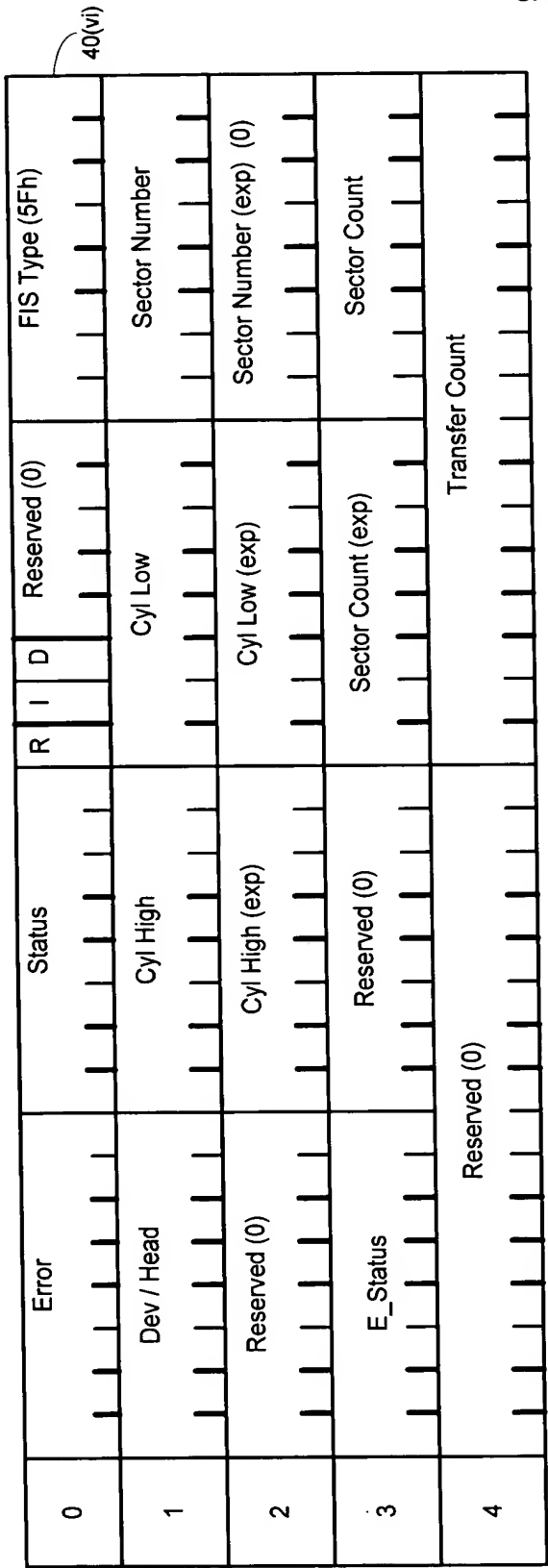


FIG. 1d (vi) PIO Setup FIS

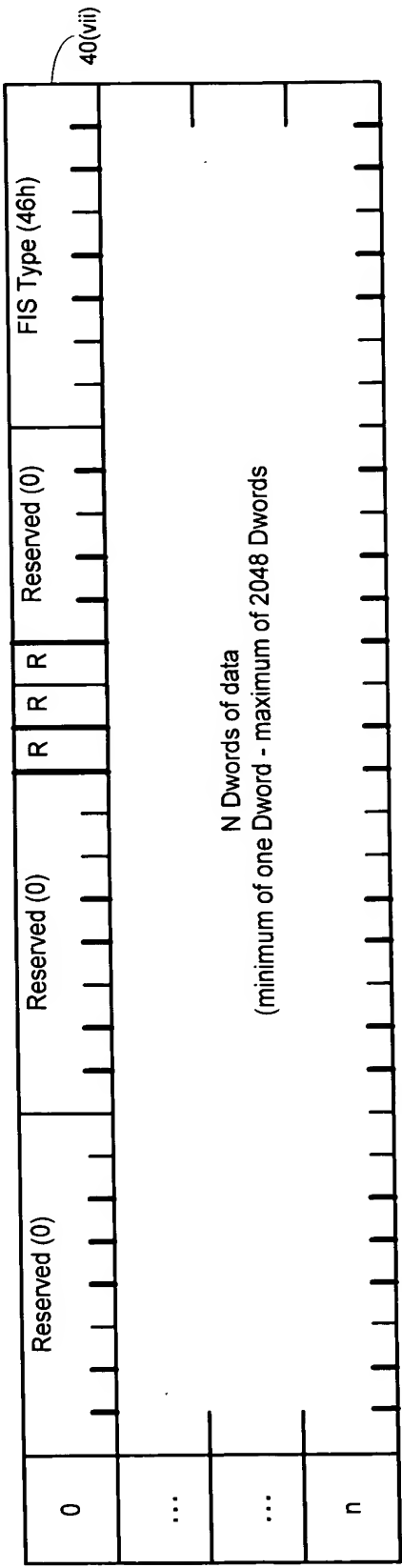


FIG. 1d(vii) Data FIS

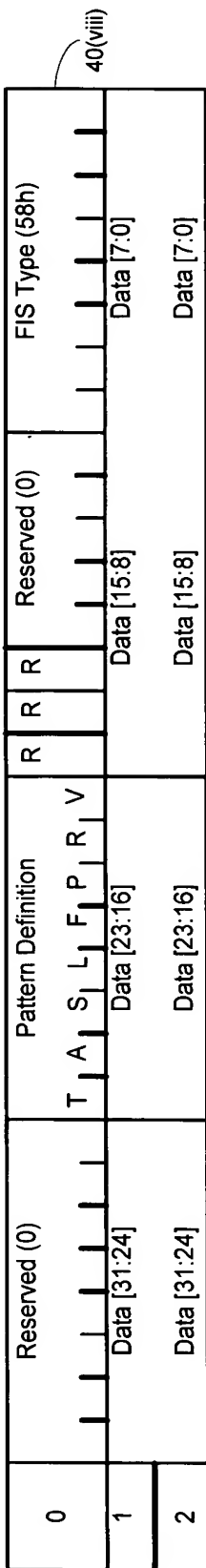
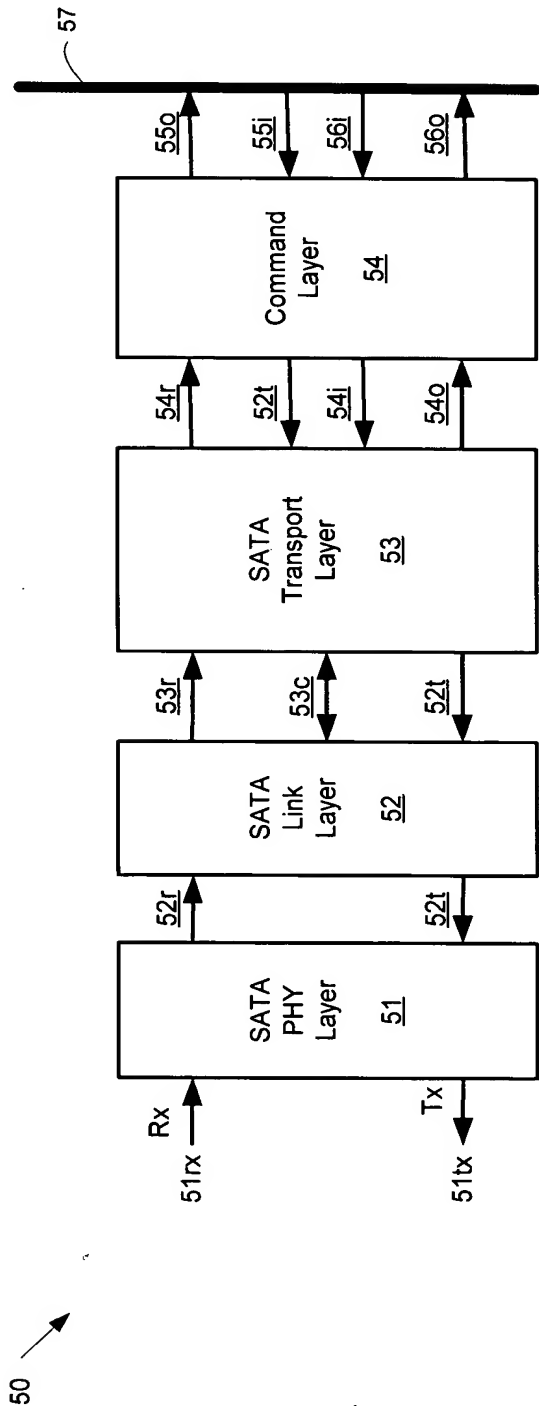


FIG. 1d (viii) BIST Activate FIS



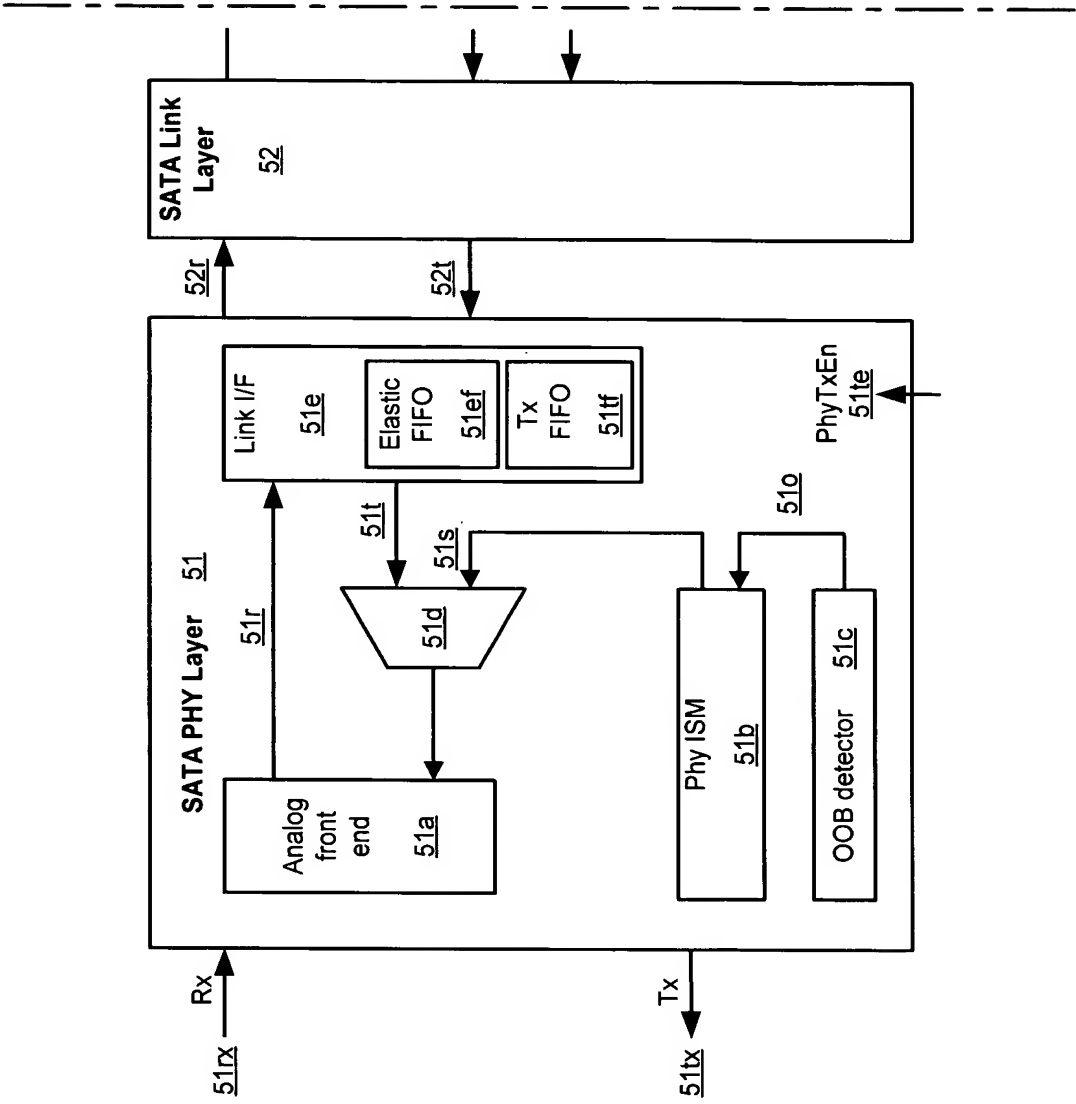


FIG. 2b(i)

Fig. 2b(i)	Fig. 2b(ii)
------------	-------------

Key To  
FIG. 2b



9/43

50

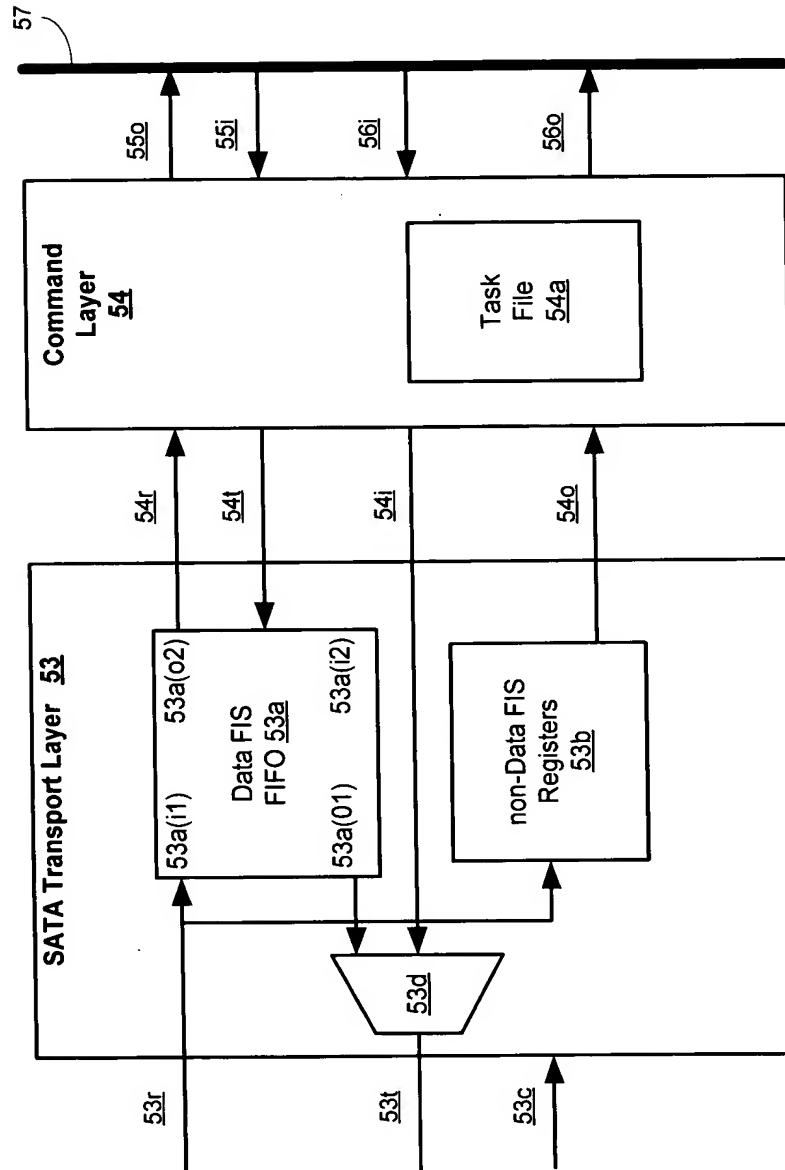


FIG. 2b(ii)

10/43

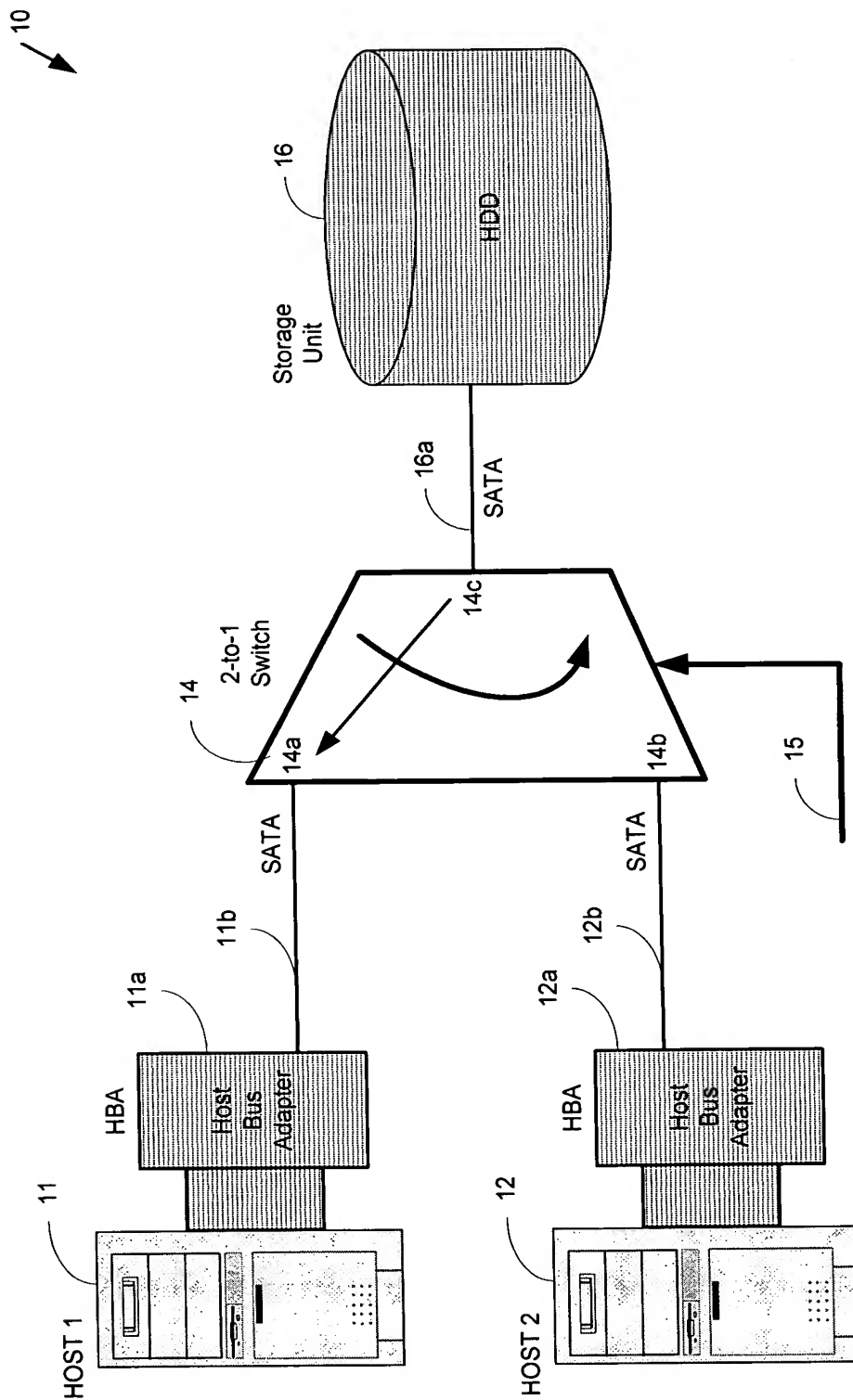


FIG. 3a

11/43

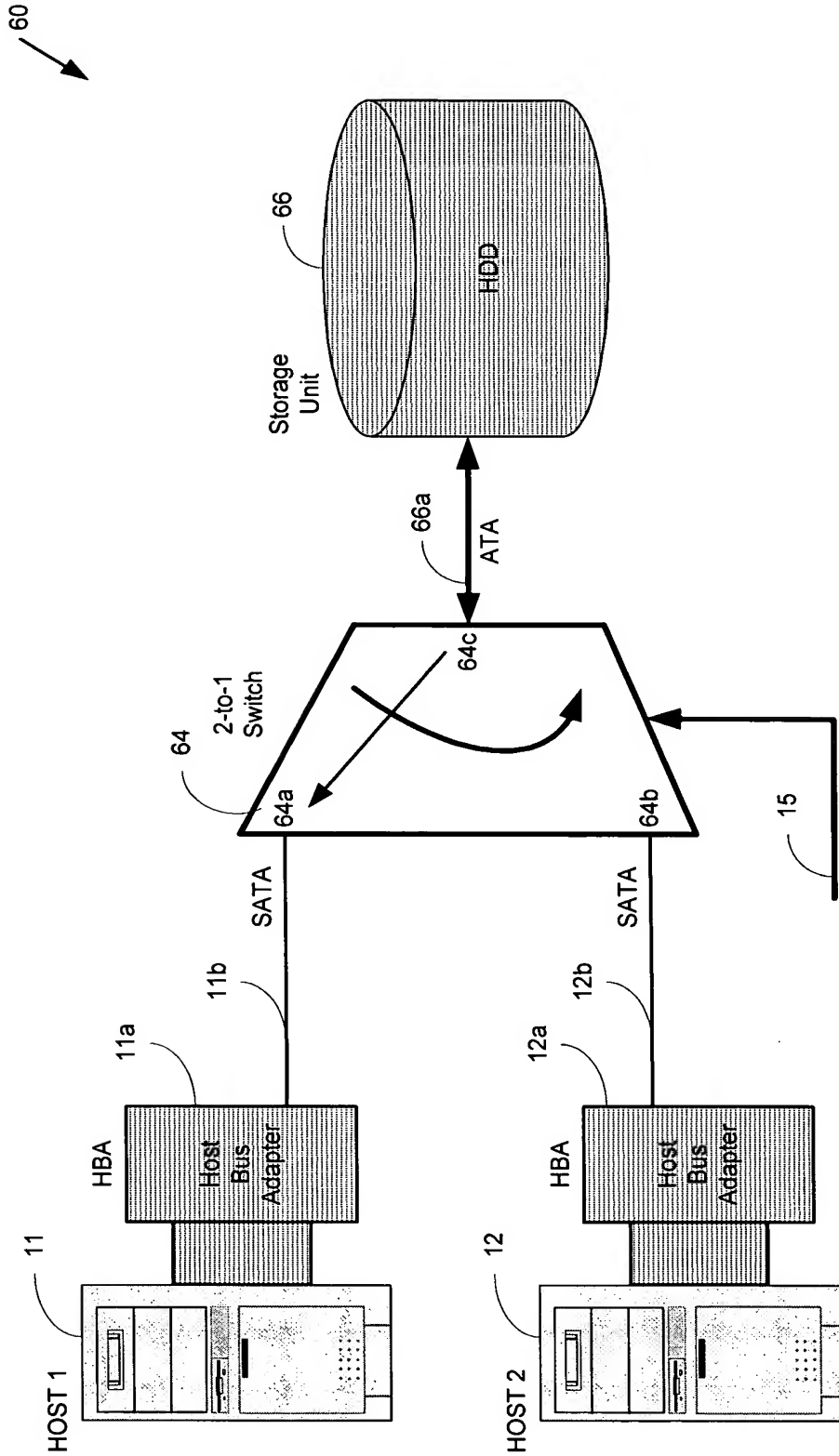


FIG. 3b

FIG. 4a

FIG. 4b

Key To

FIG. 4

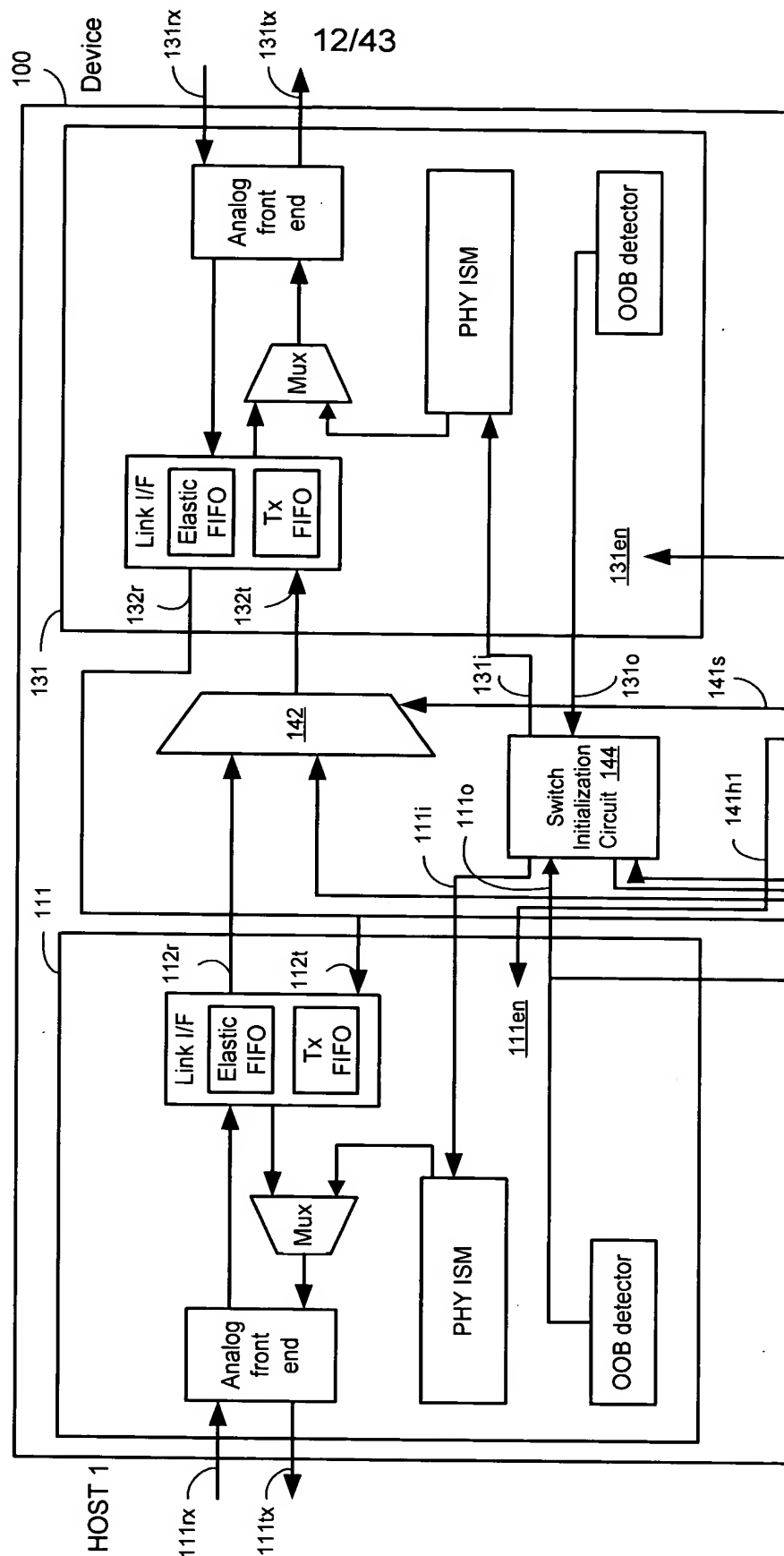


FIG. 4a

13/43

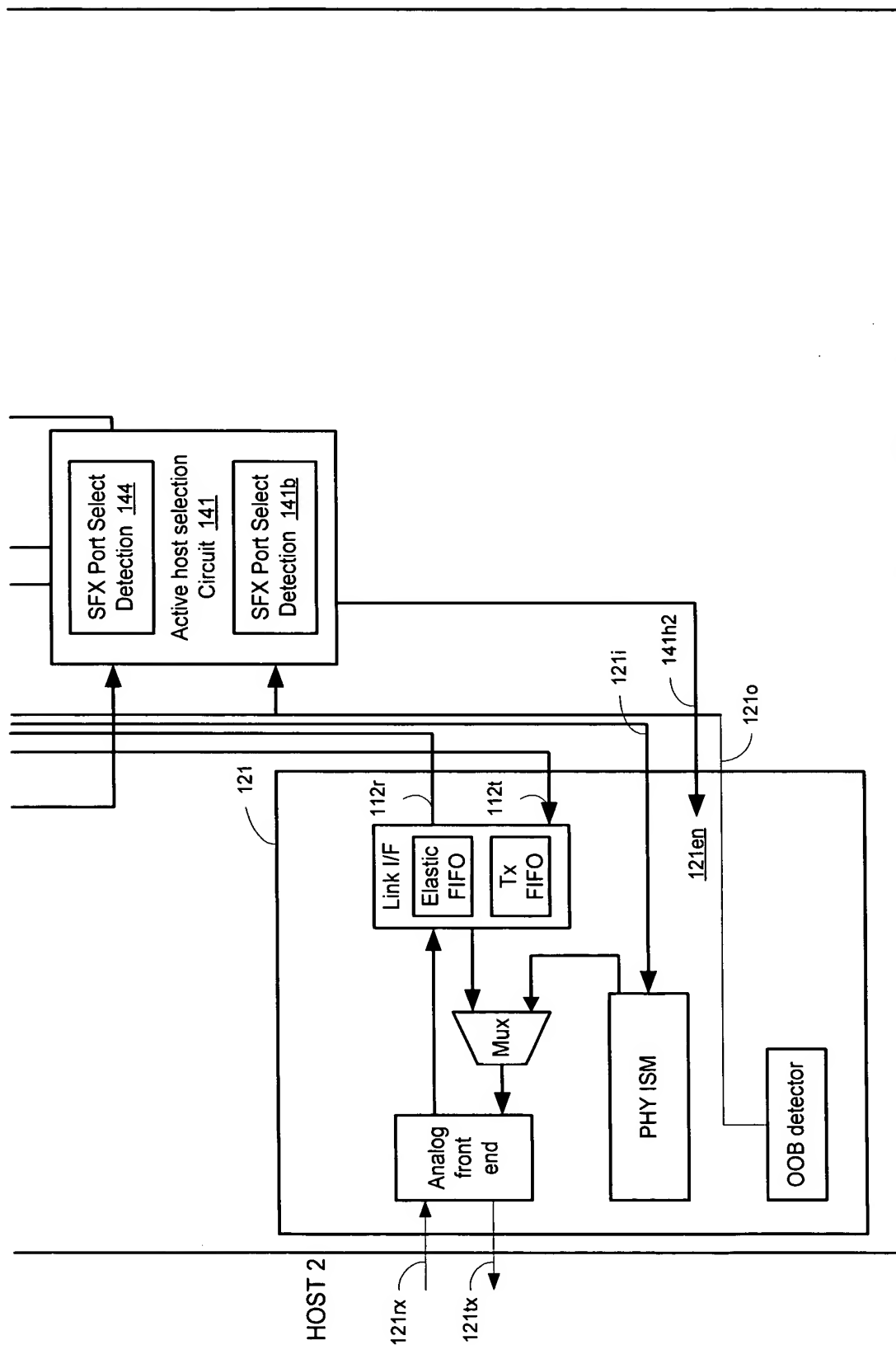


FIG. 4b

FIG. 5a	FIG. 5b
FIG. 5c	Key To FIG. 5

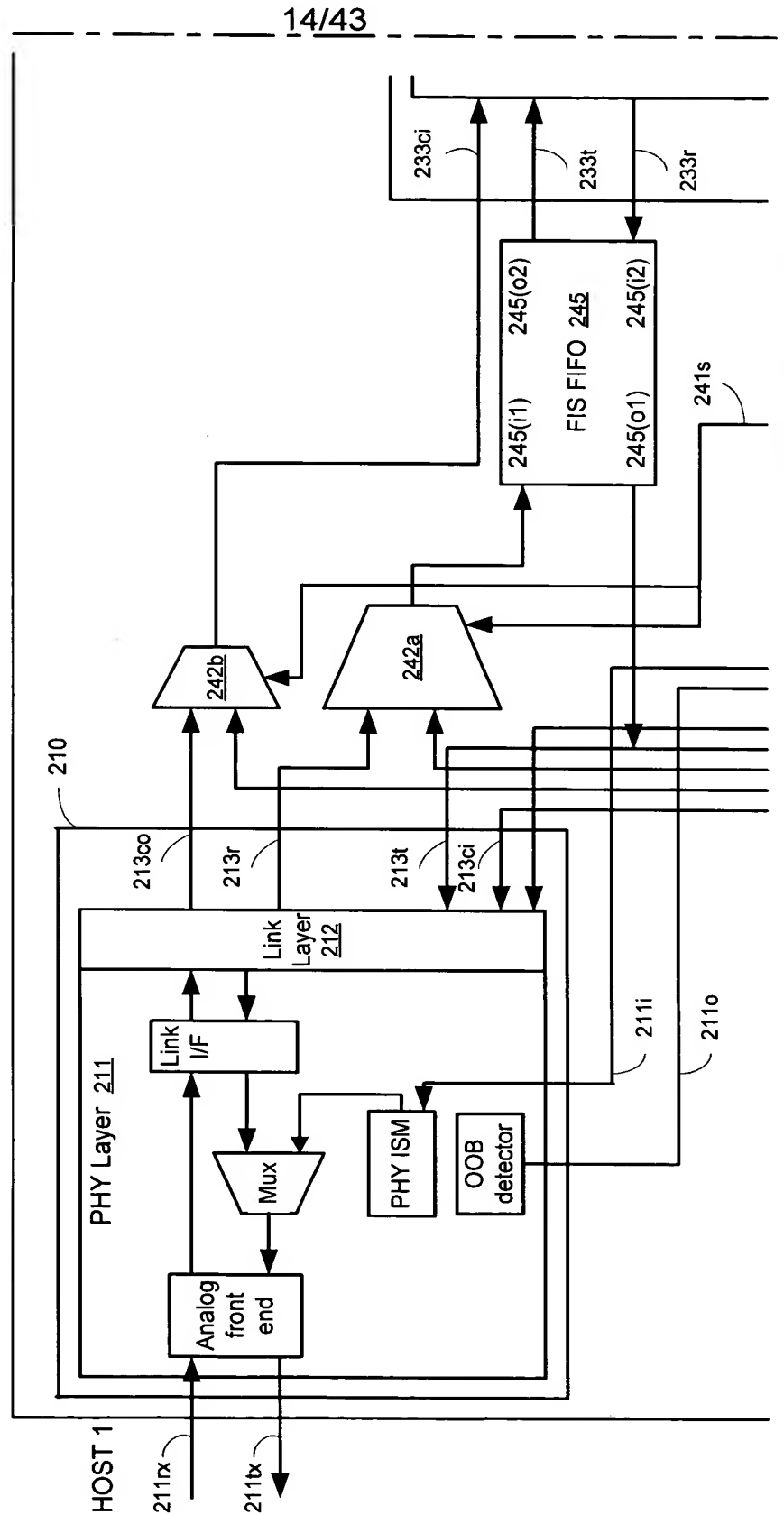


FIG. 5a

15/43

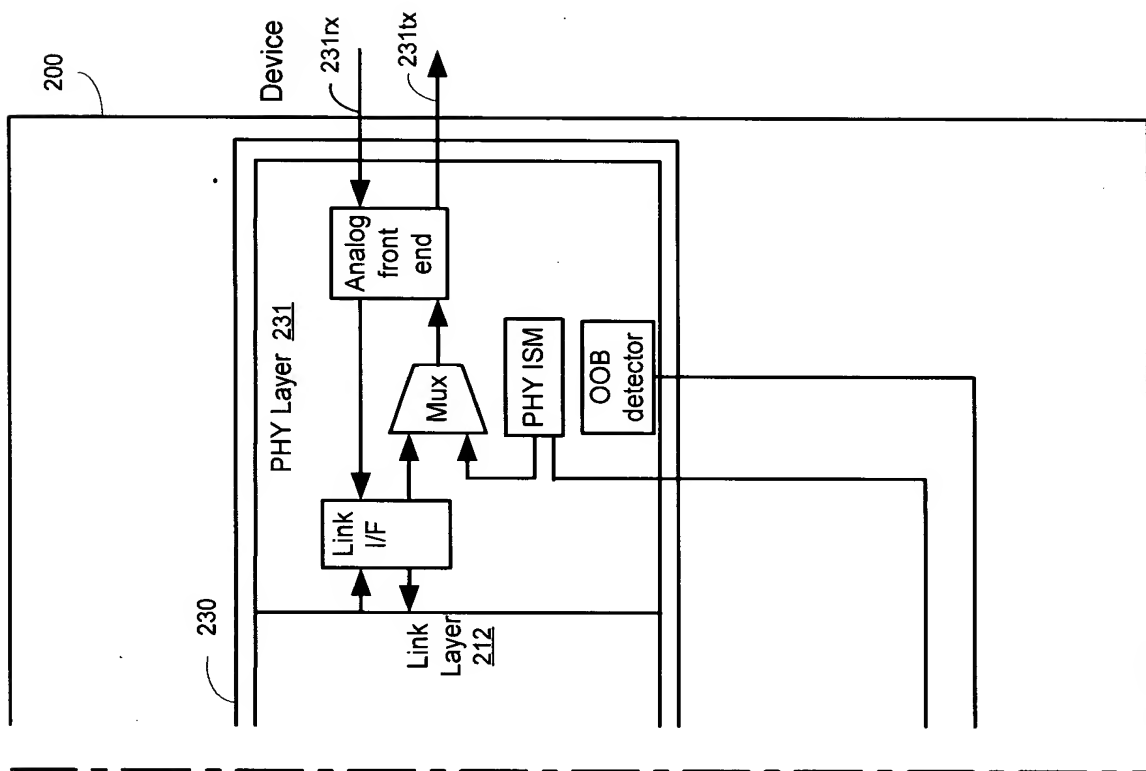


FIG. 5b

16/43

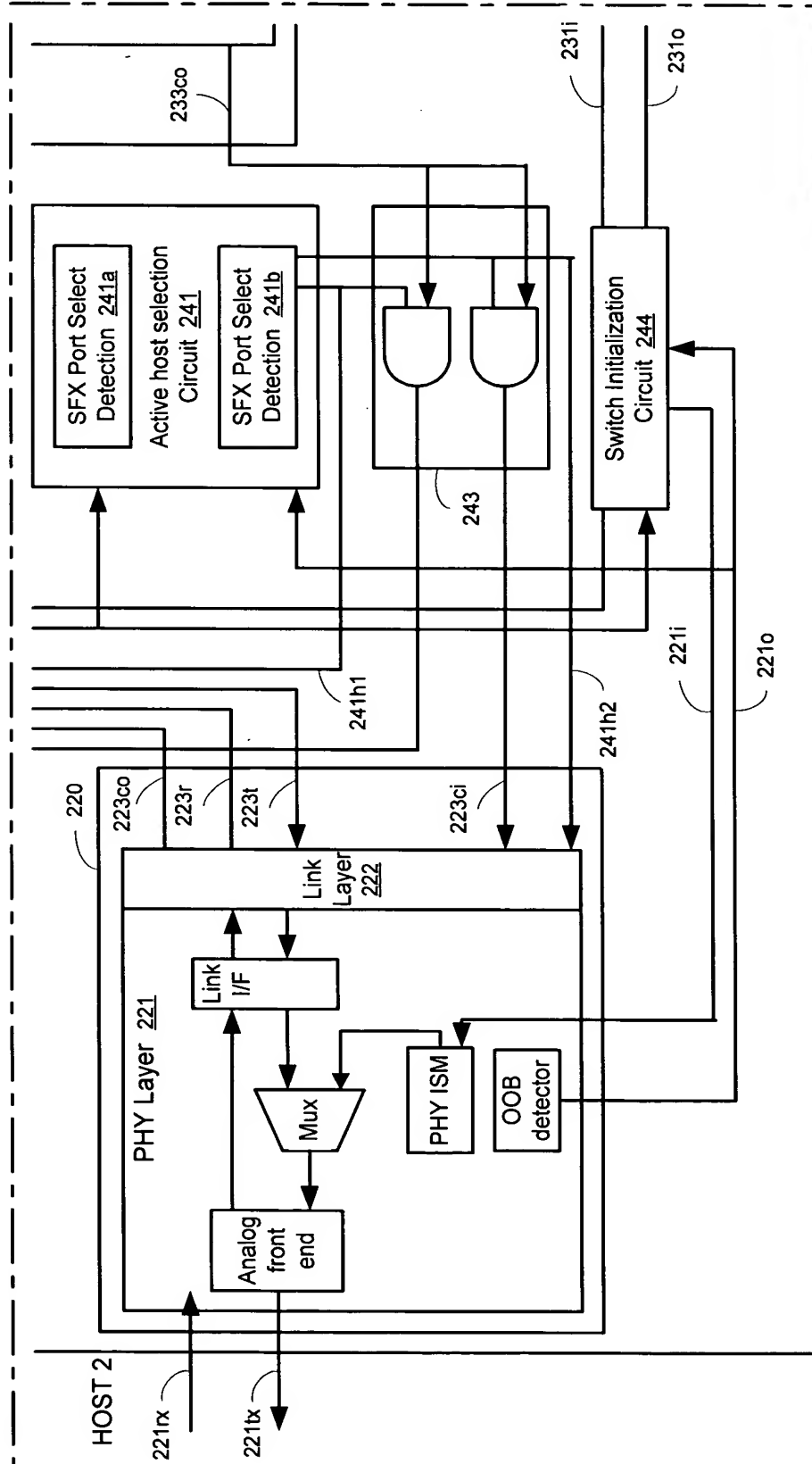


FIG. 5c



FIG. 6a      FIG. 6b

Key To  
FIG. 6

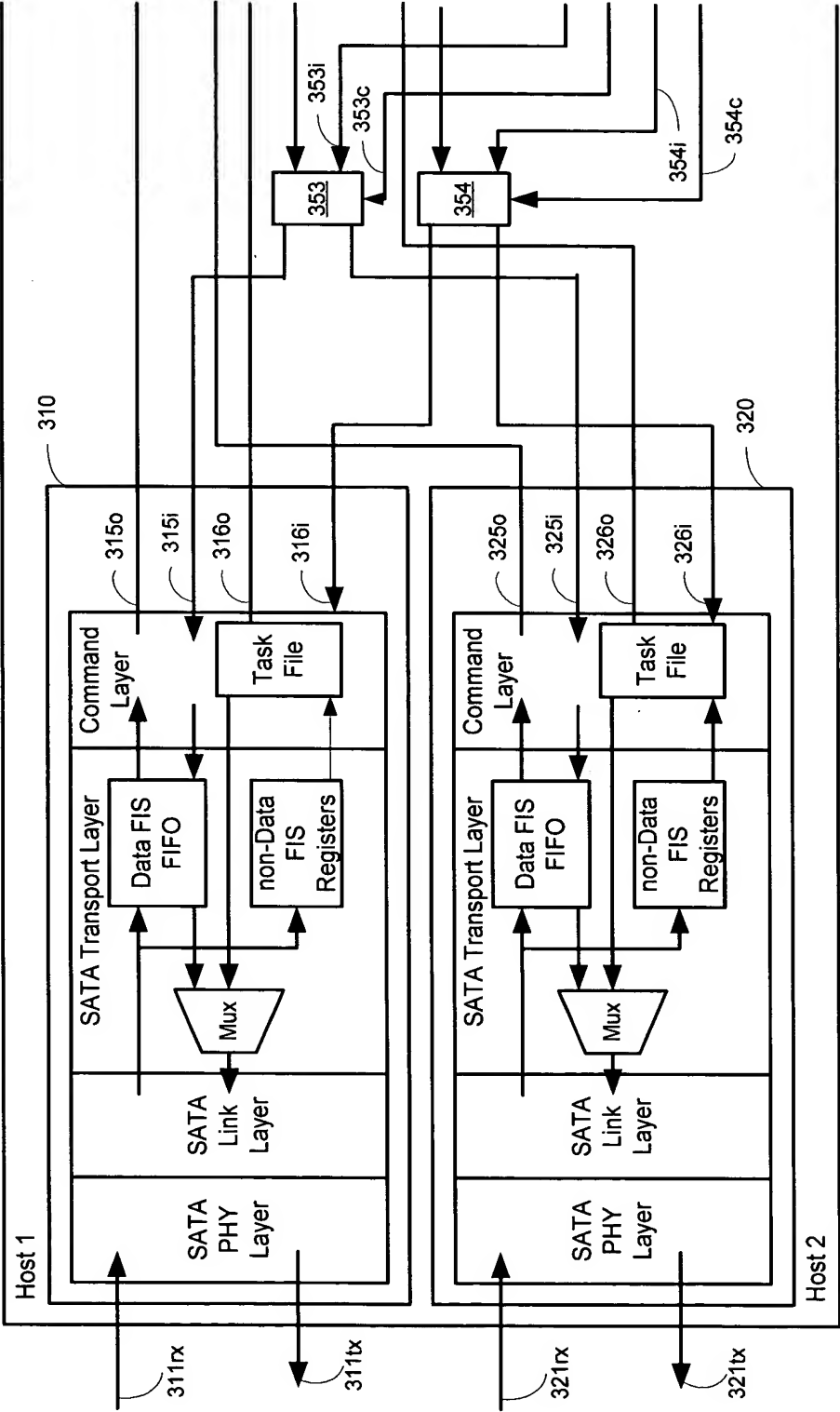


FIG. 6a

18/43

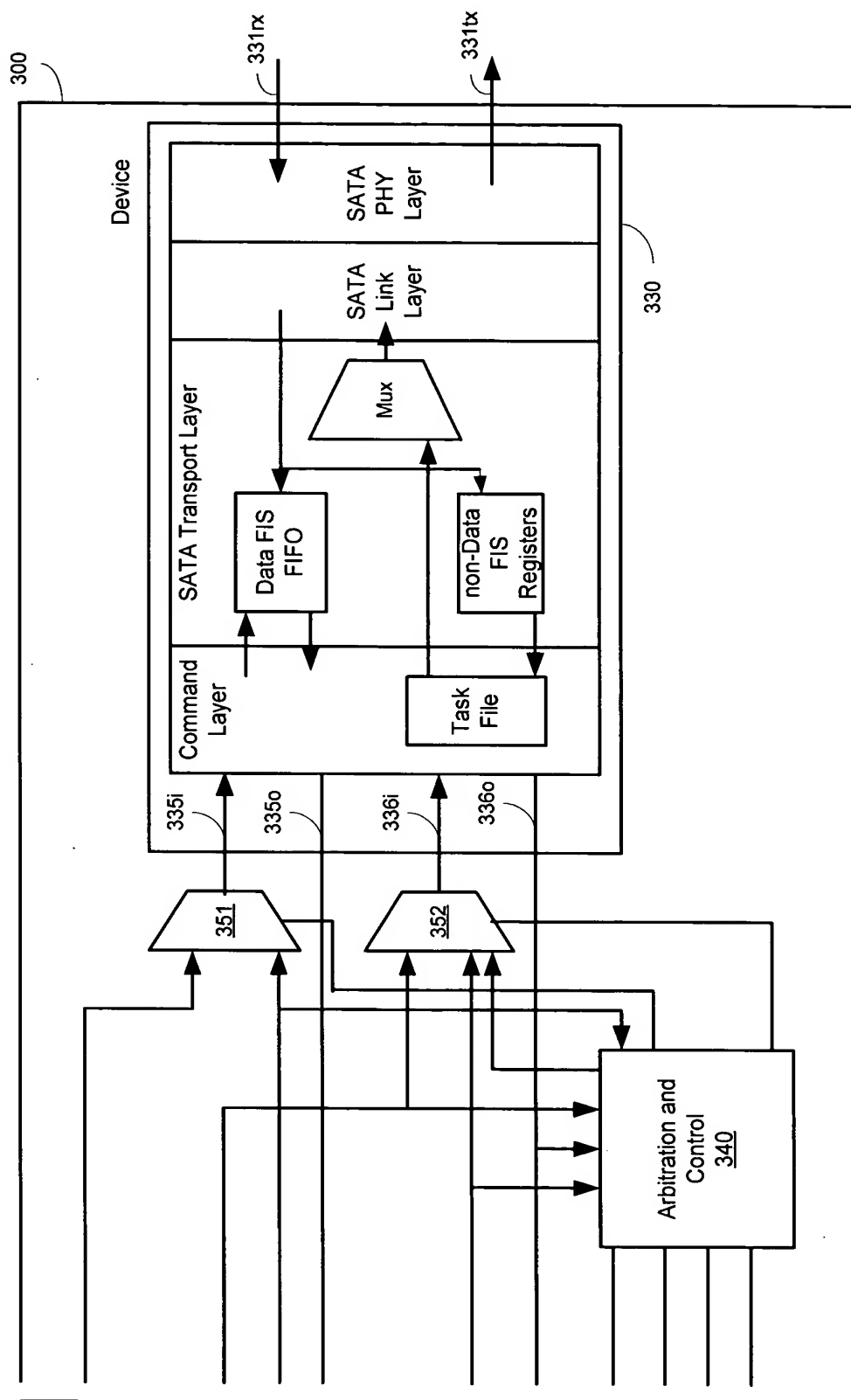


FIG. 6b

19/43

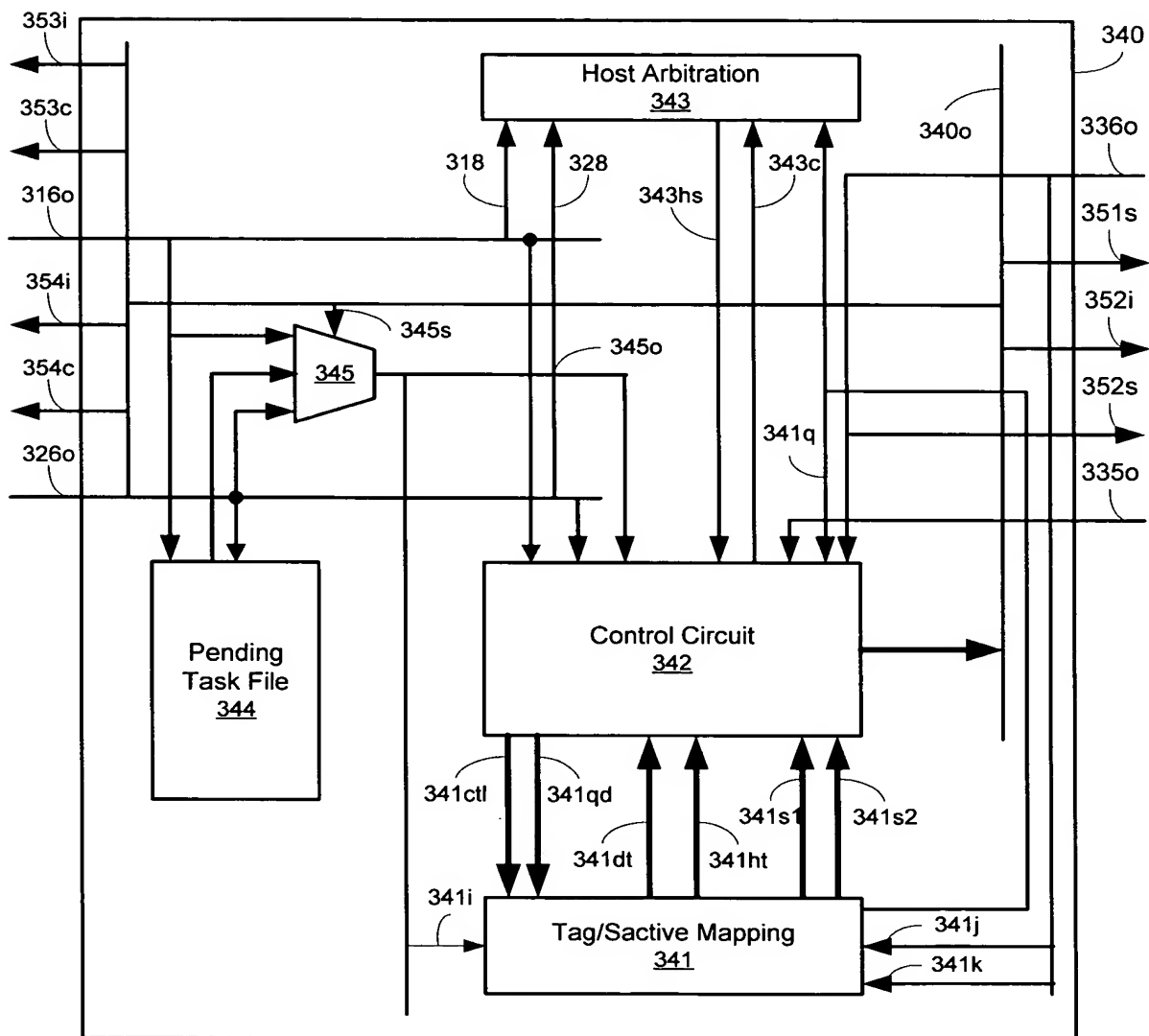


FIG. 7a

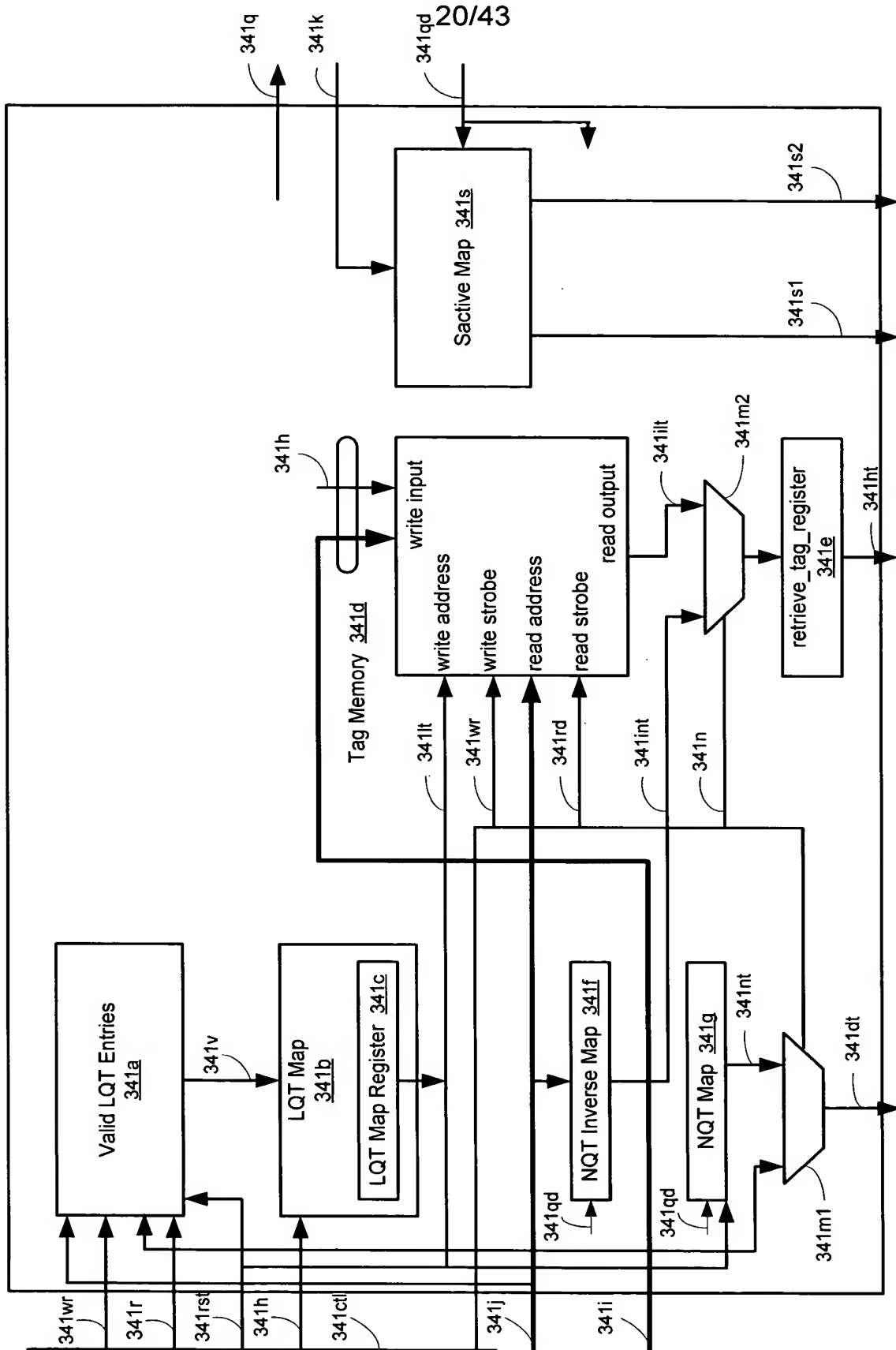


FIG. 7b

21/43

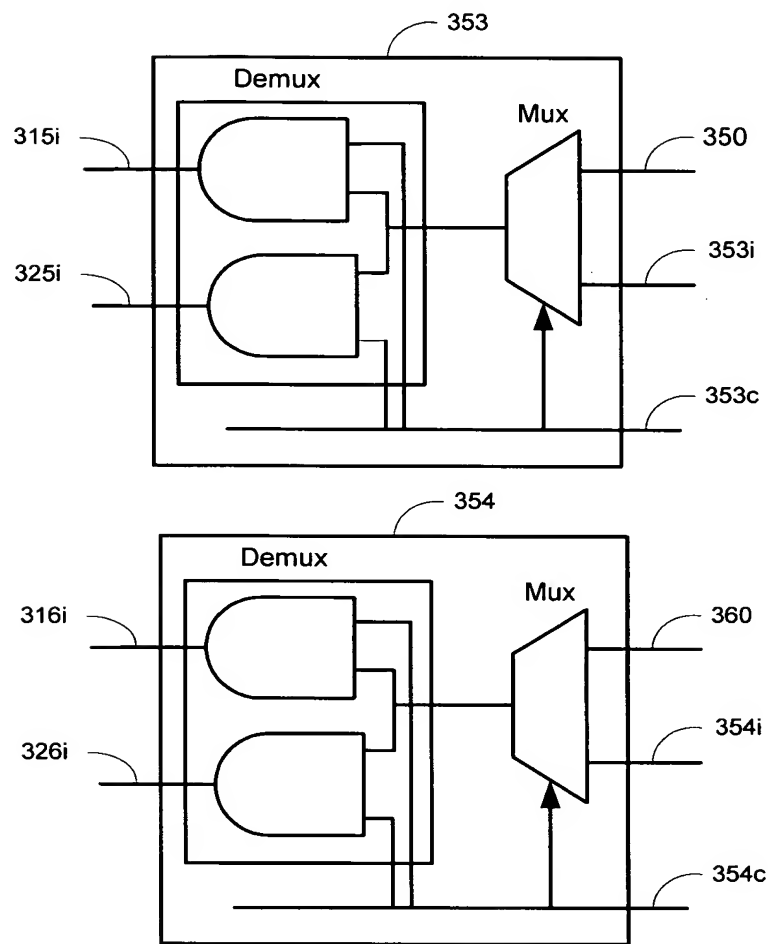


FIG. 7c

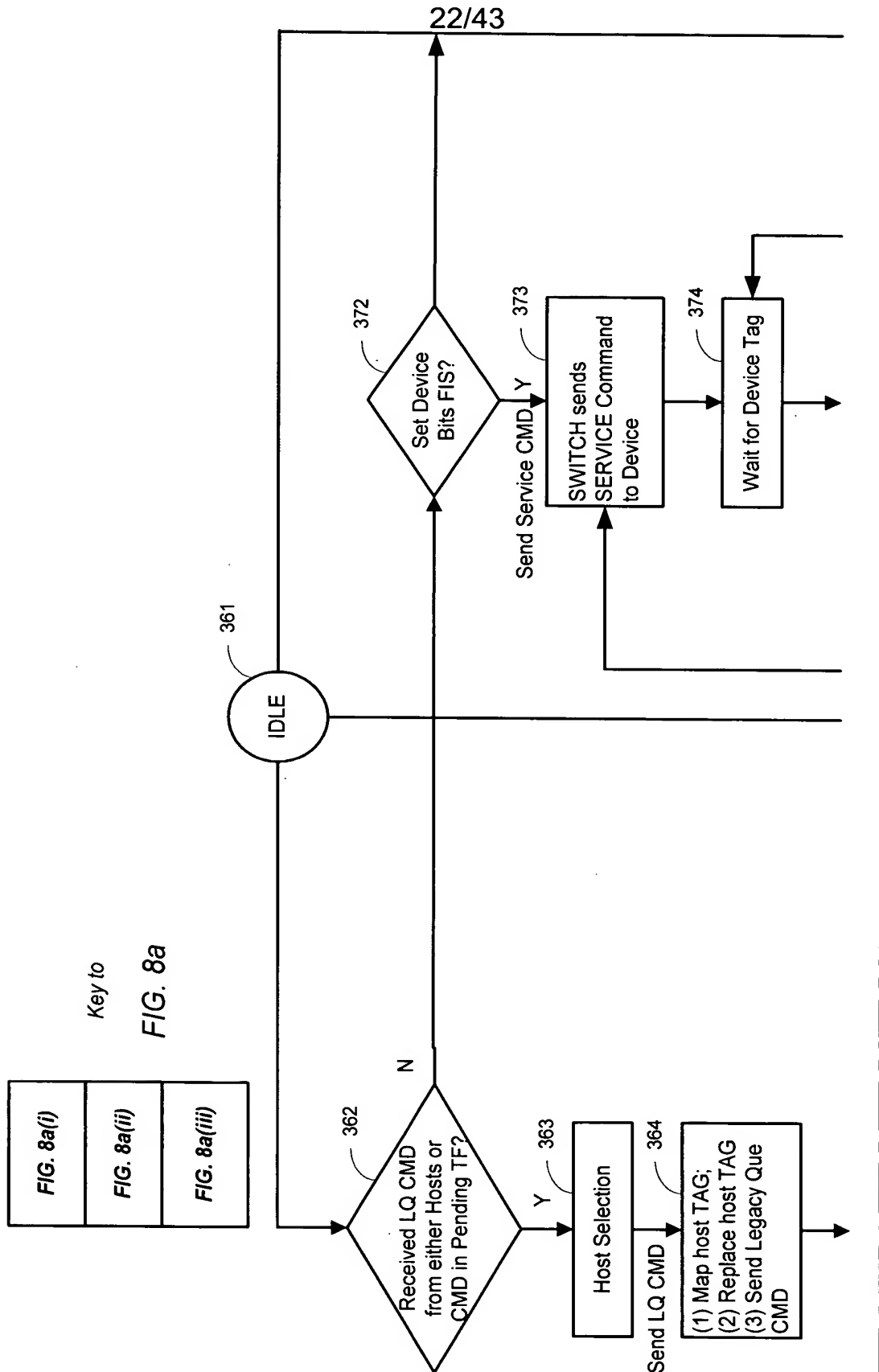


FIG. 8a(i)

23/43

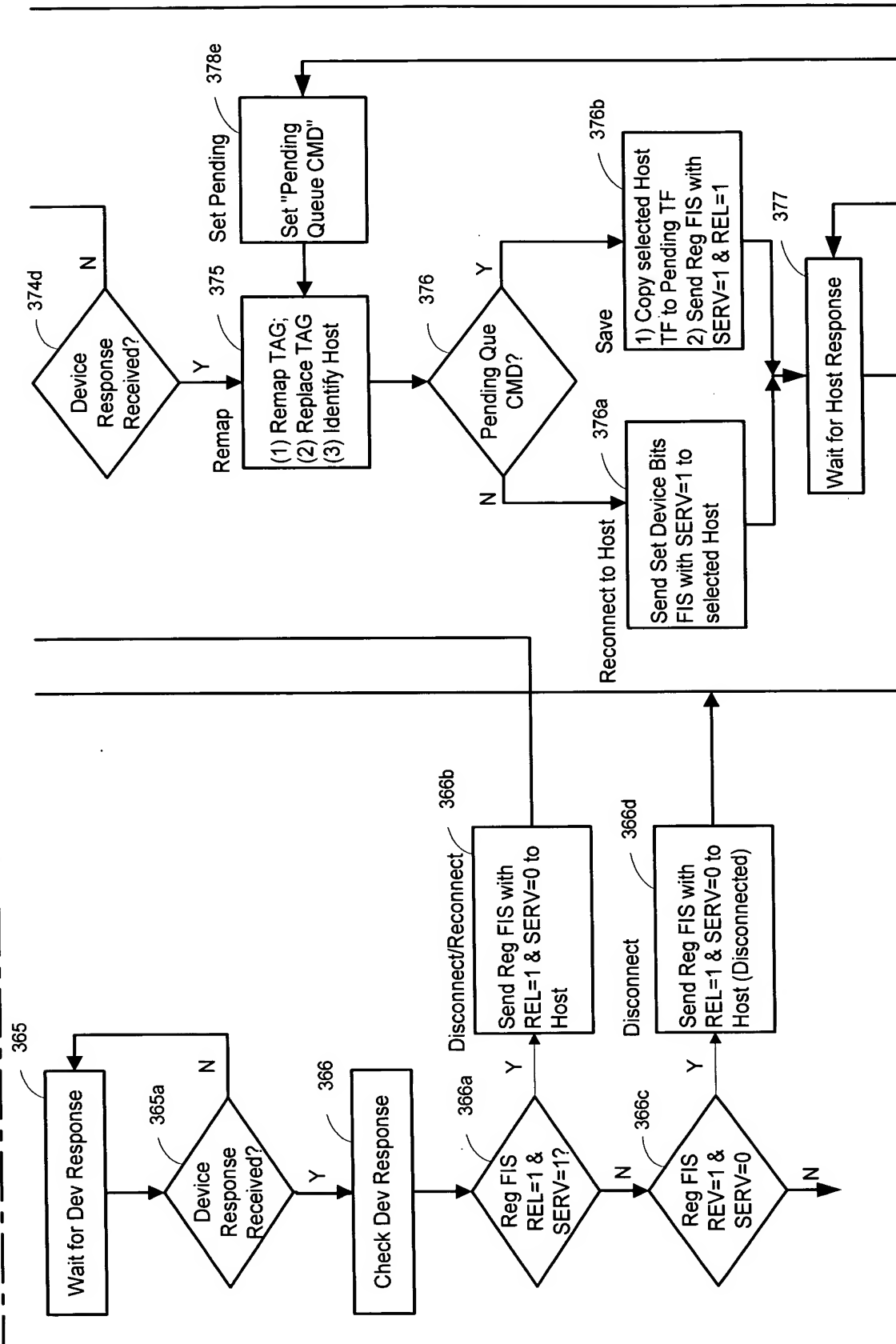


FIG. 8a(ii)

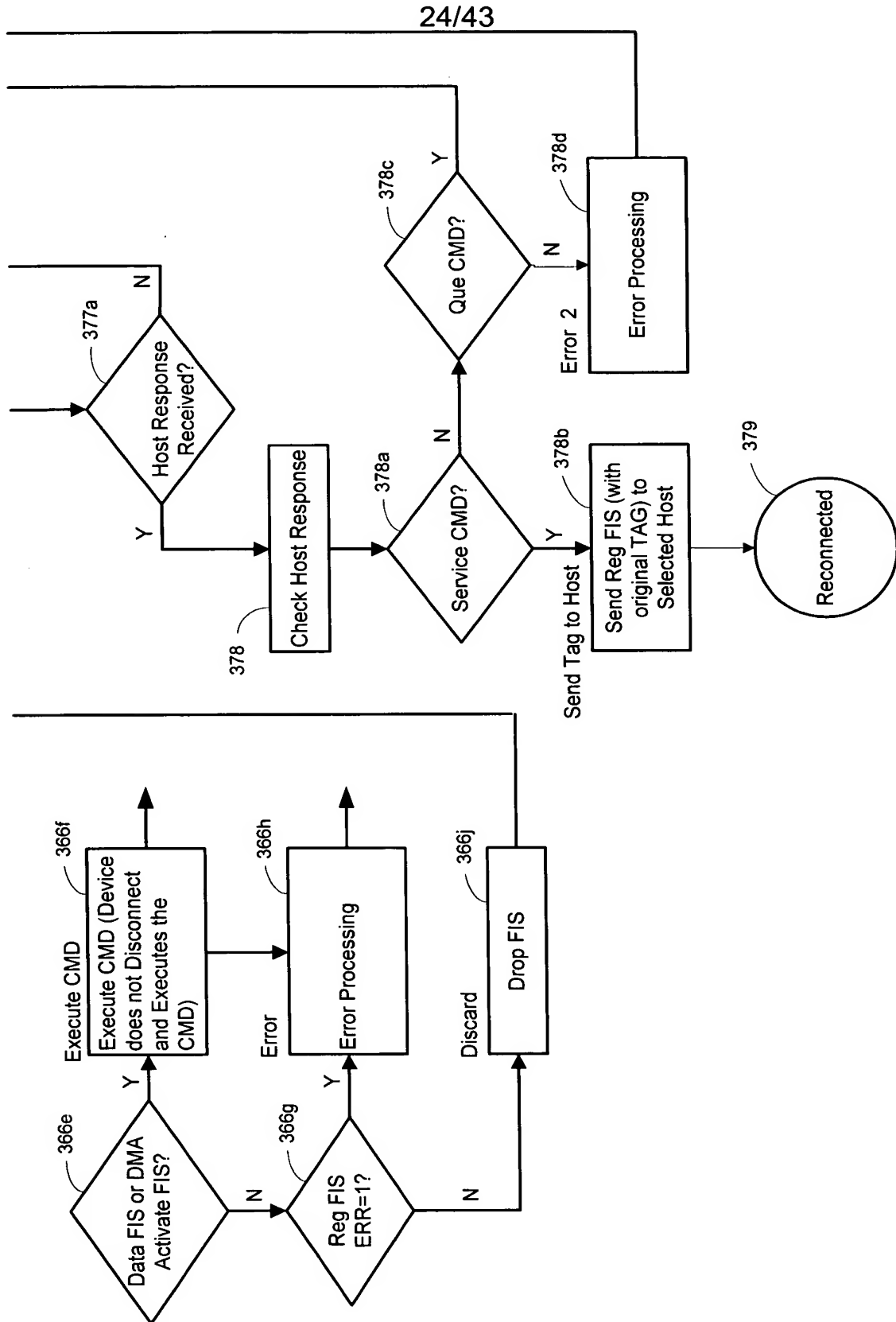


FIG. 8a(iii)



25/43

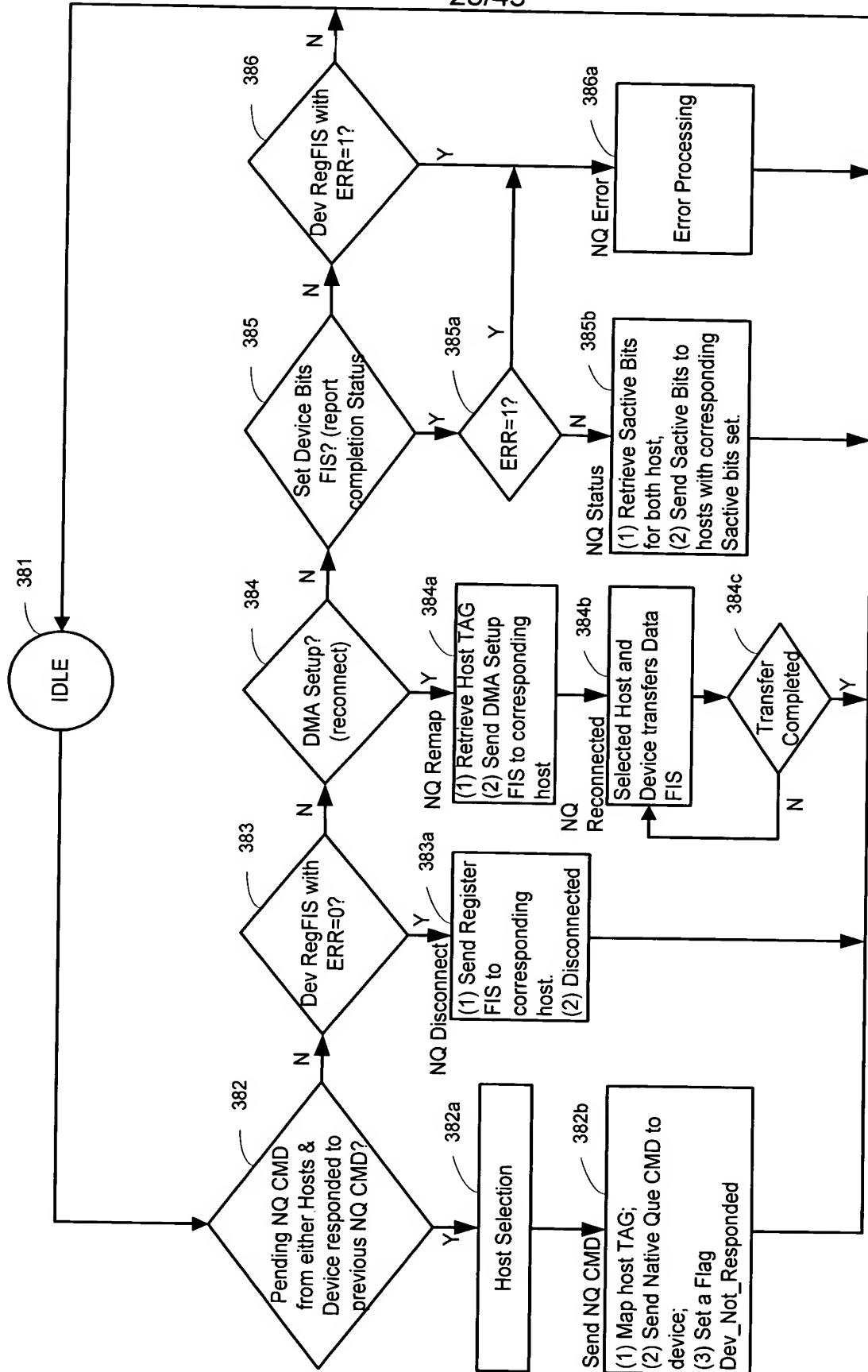


FIG. 8b

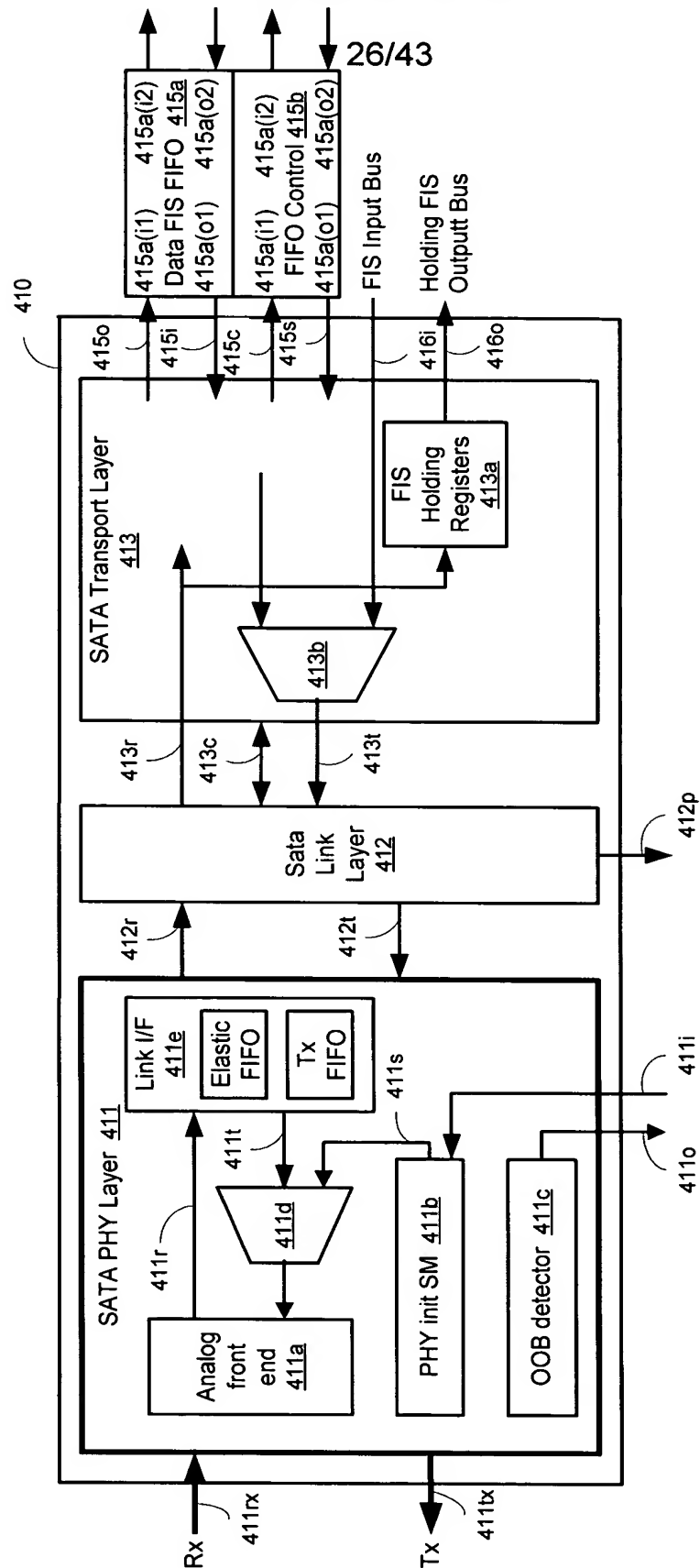


FIG. 9

27/43

FIG. 10a(i)	FIG. 10a(ii)	FIG. 10a(iii)
-------------	--------------	---------------

Key to

FIG. 10a

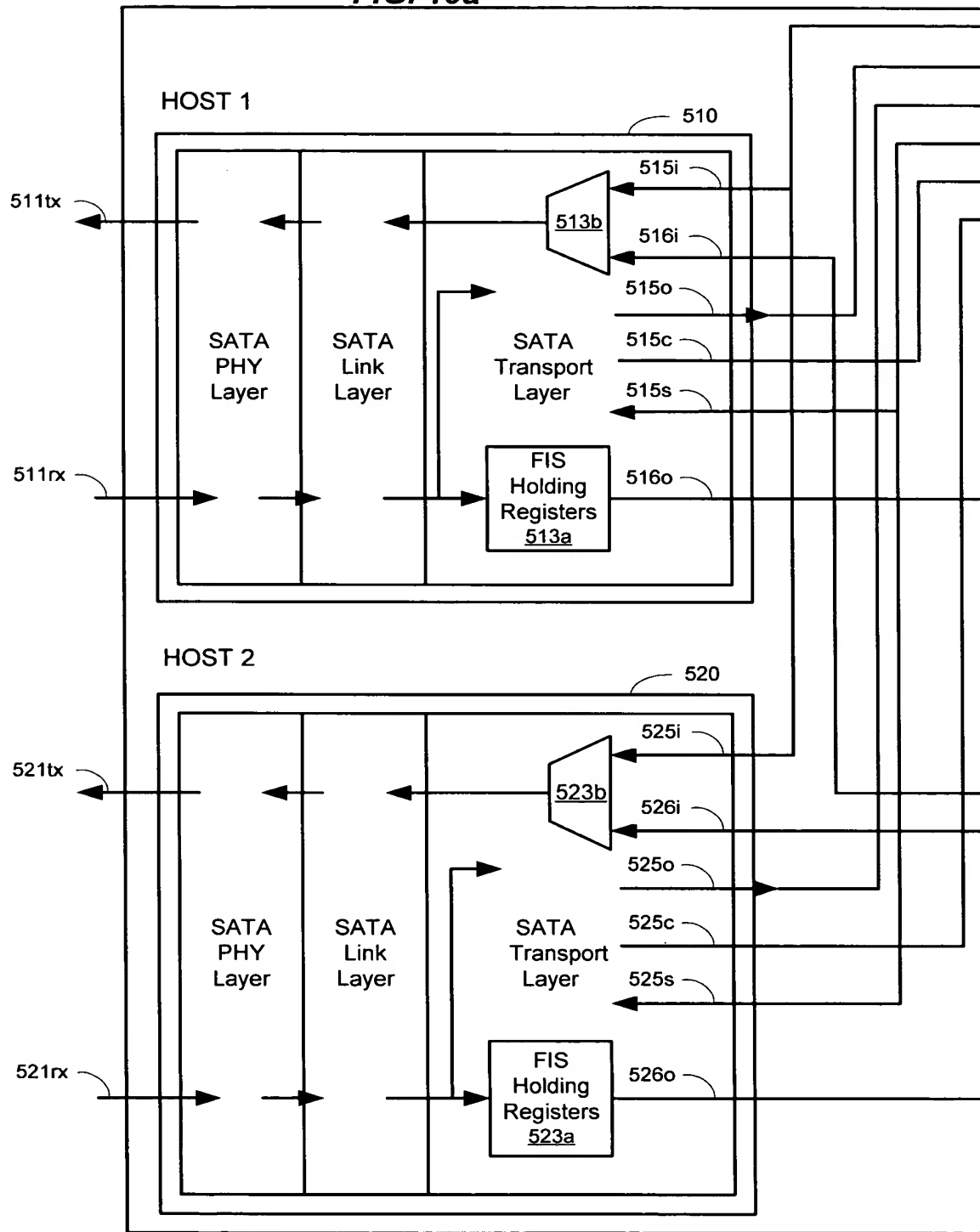


FIG. 10a(i)

28/43

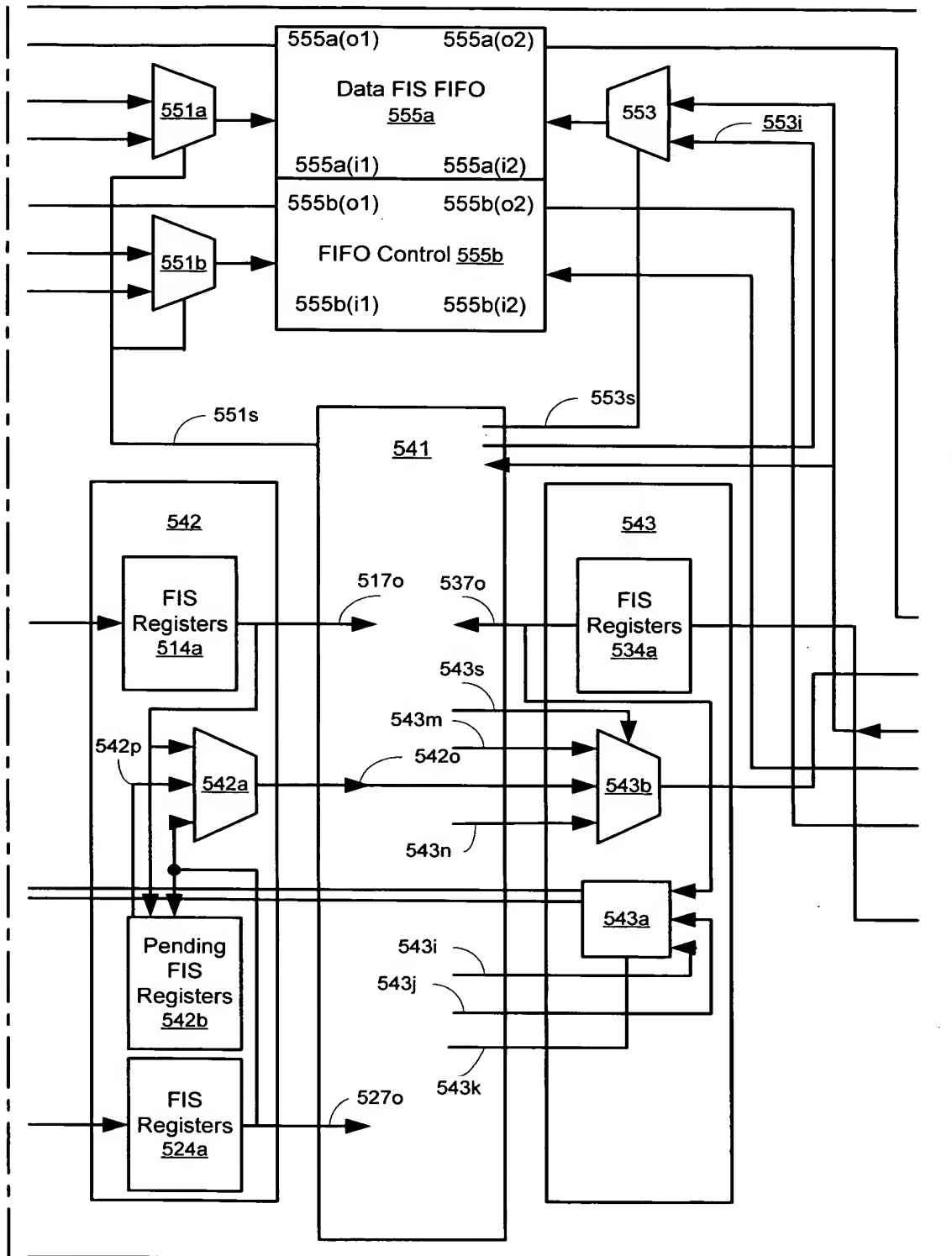


FIG. 10a(ii)

29/43

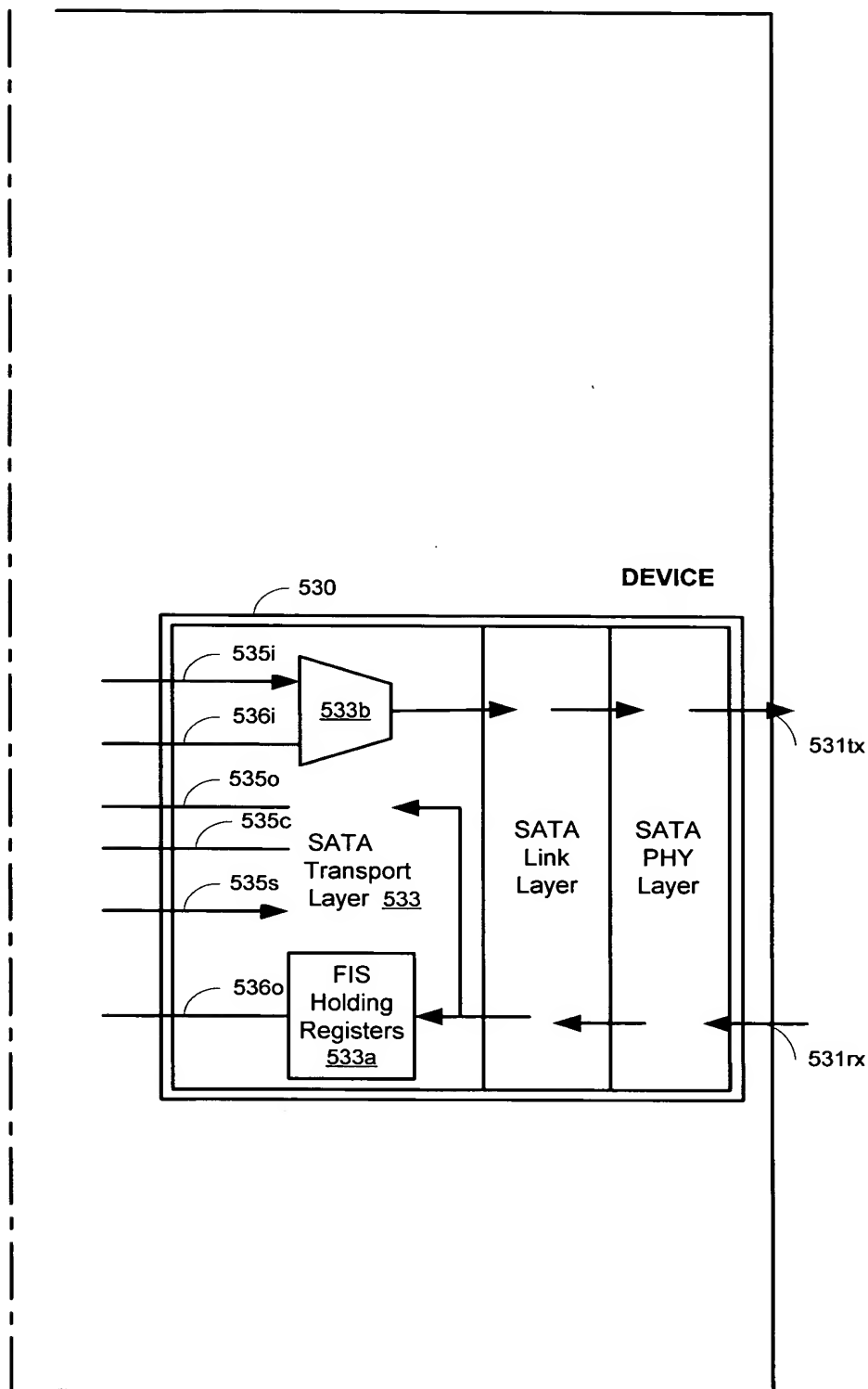


FIG. 10a(iii)

30/43

FIG. 10b(i)

FIG. 10b(ii)

FIG. 10b(iii)

Key to

FIG. 10b

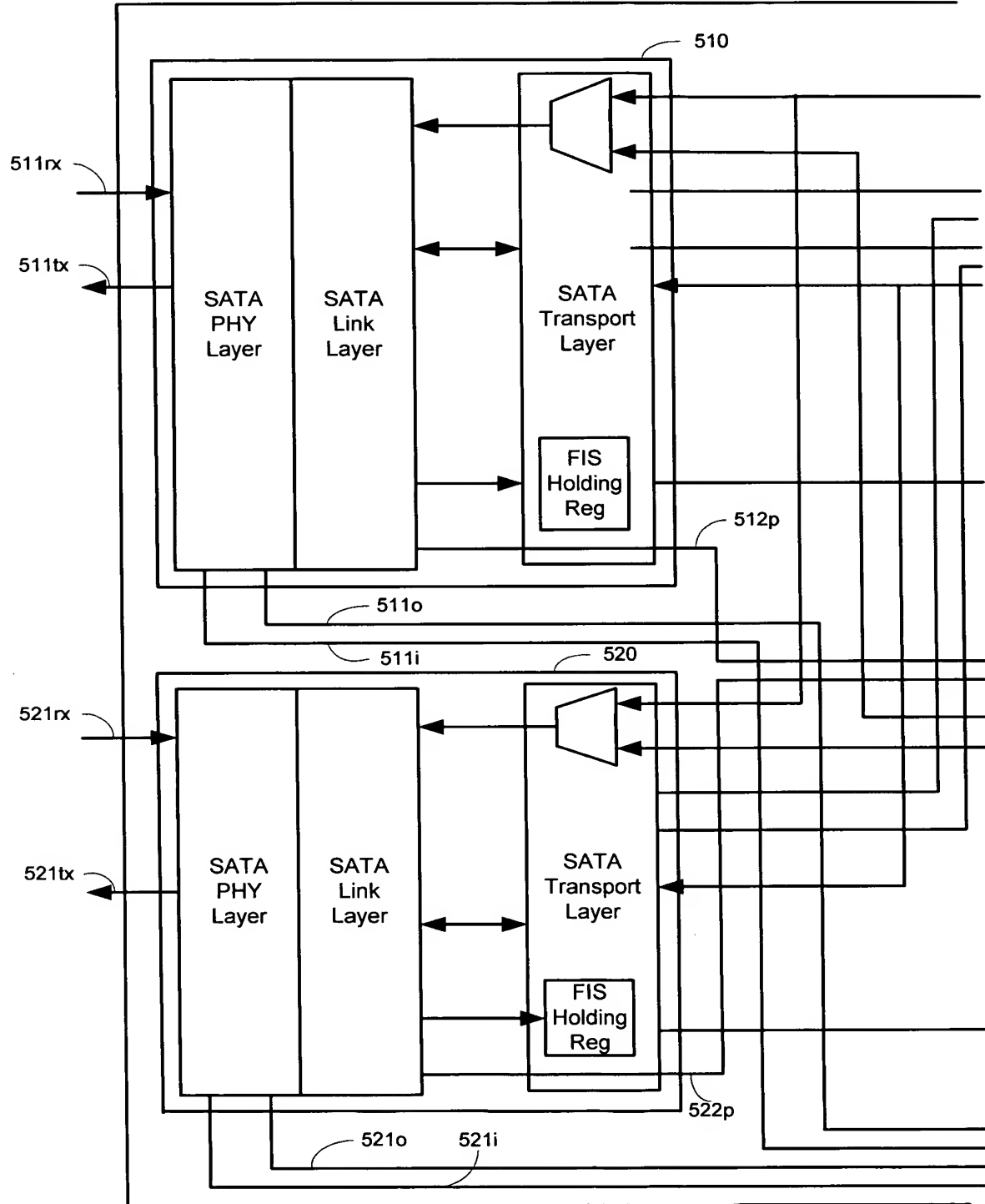


FIG. 10b(i)

31/43

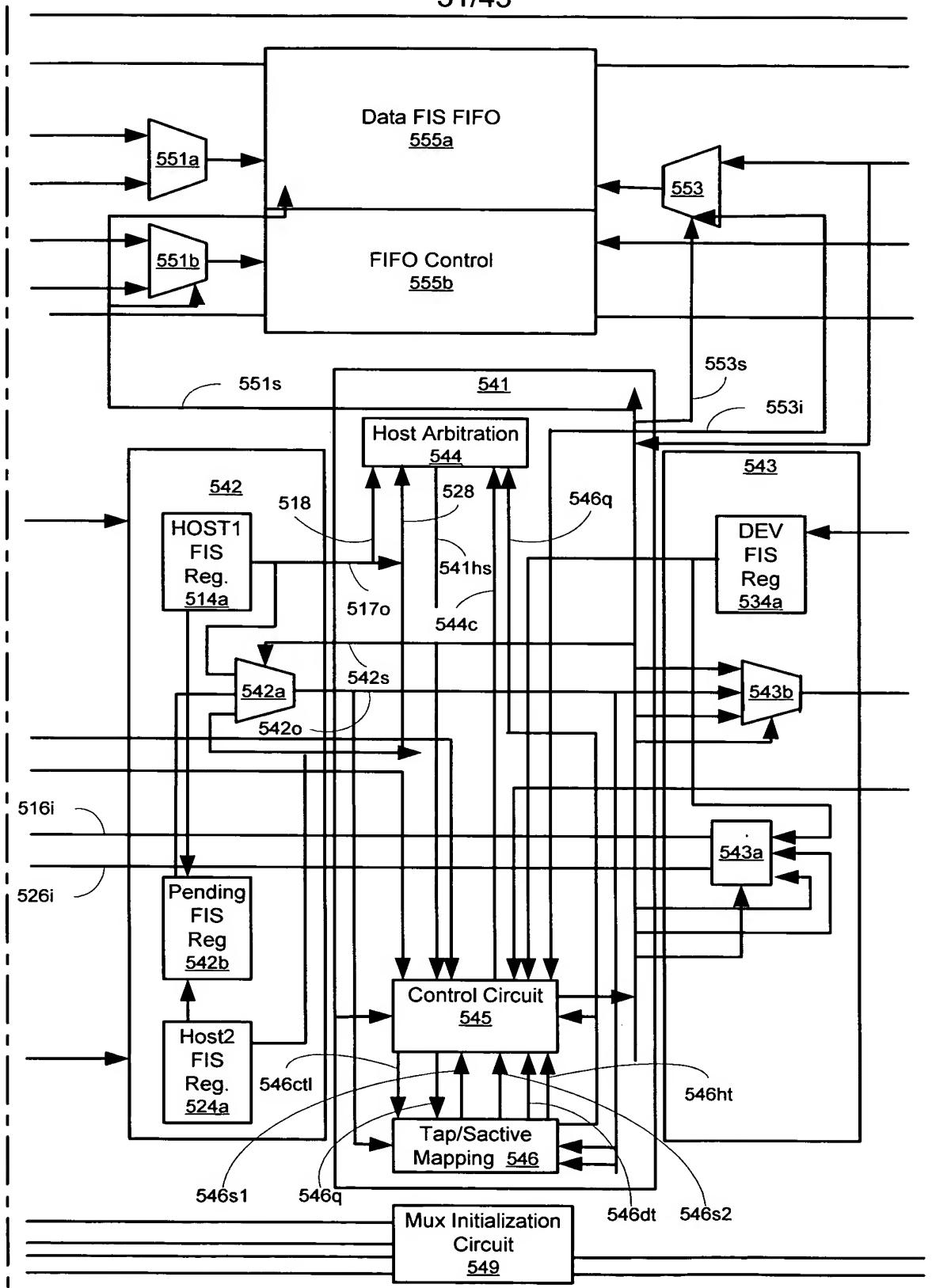


FIG. 10b(ii)

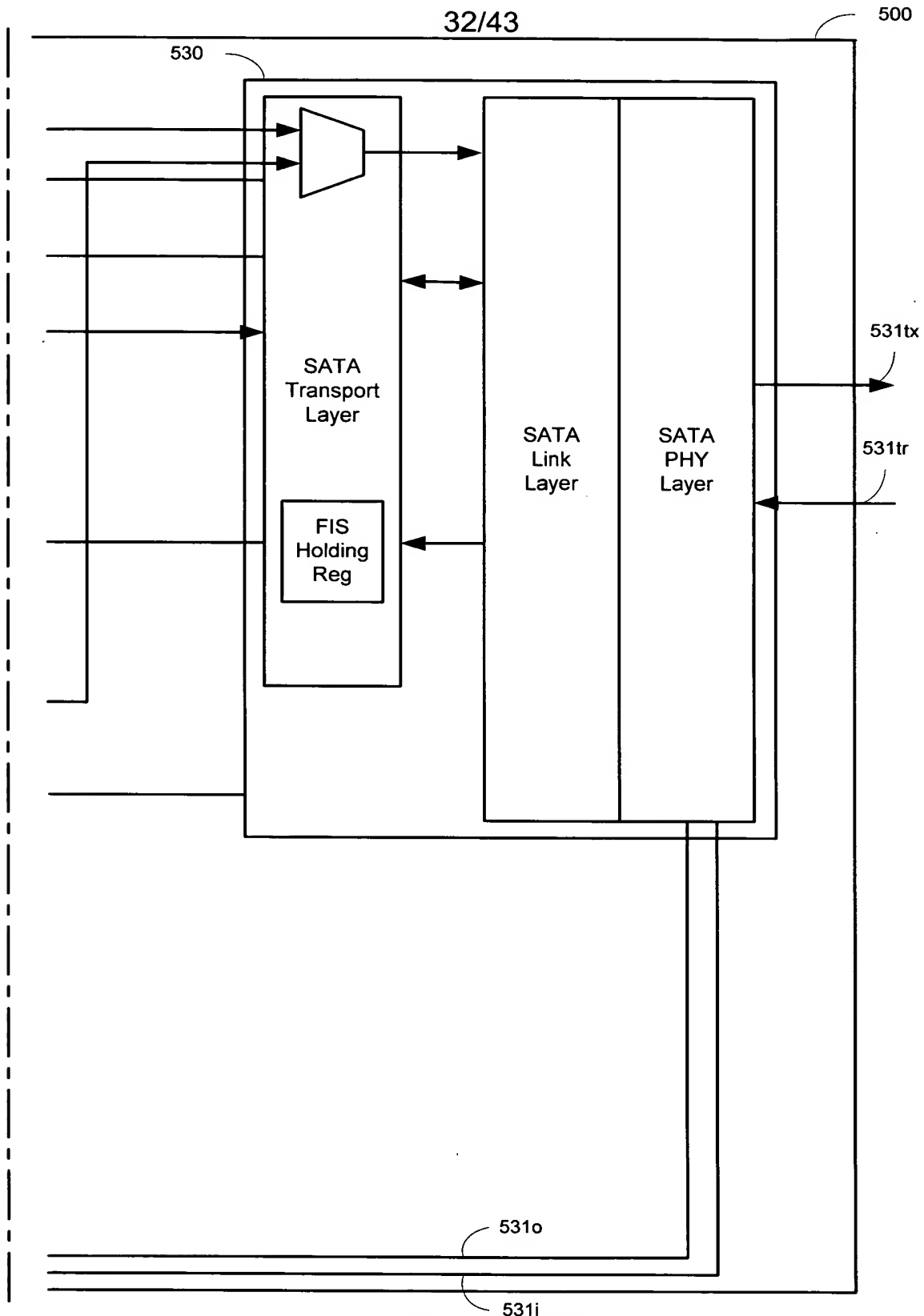


FIG. 10b(iii)



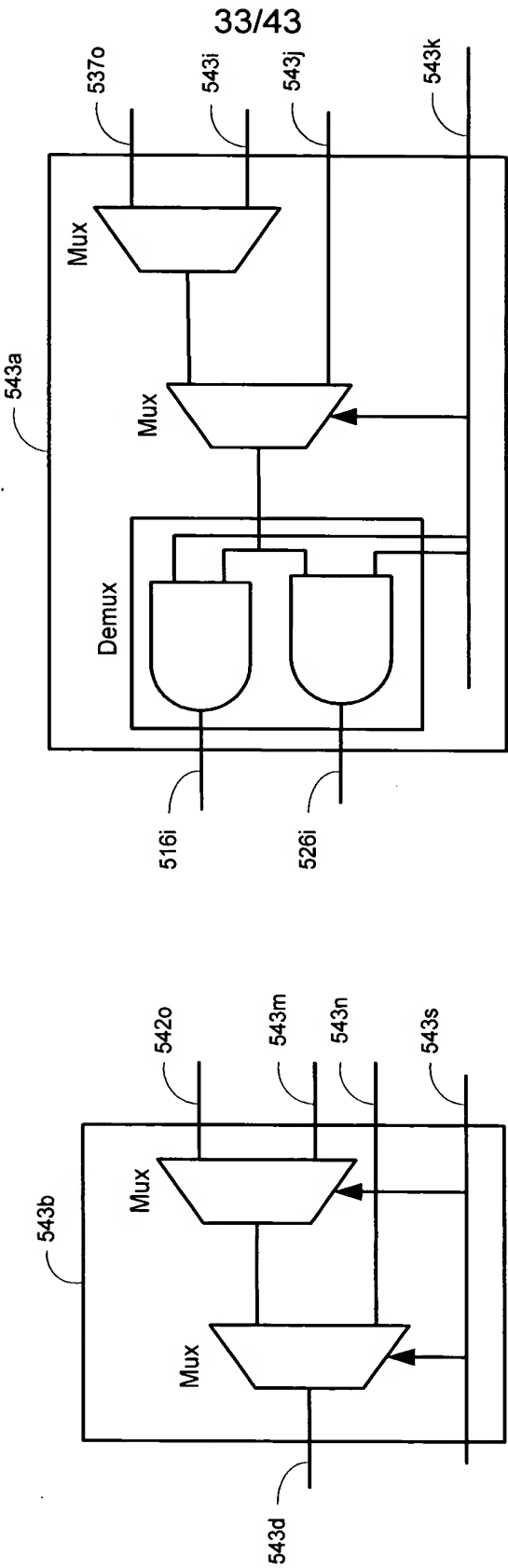
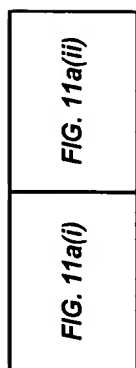


FIG. 10c

34/43



Key To

FIG. 11a

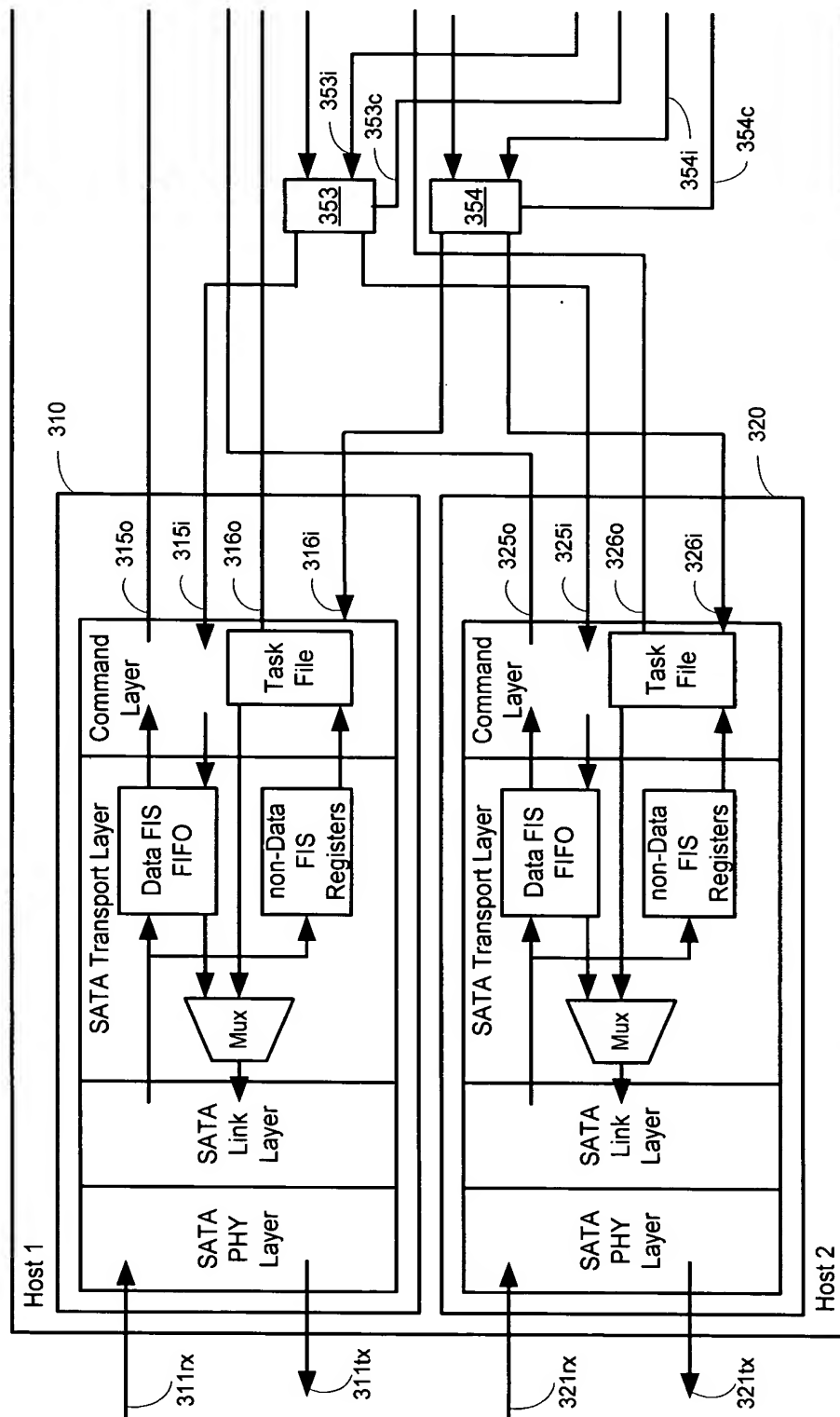


FIG. 11a(i)

35/43

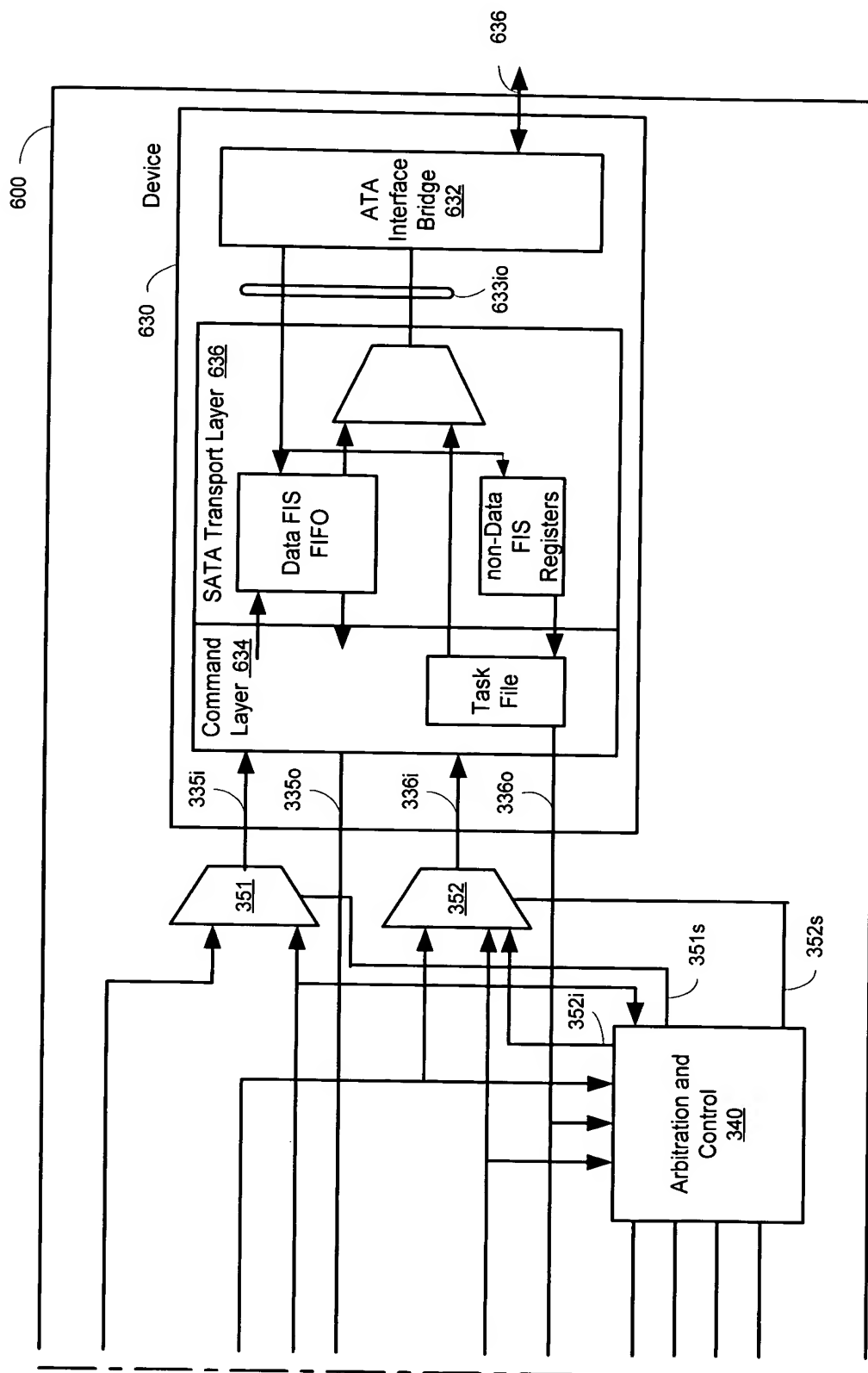


FIG. 11a(ii)

36/43

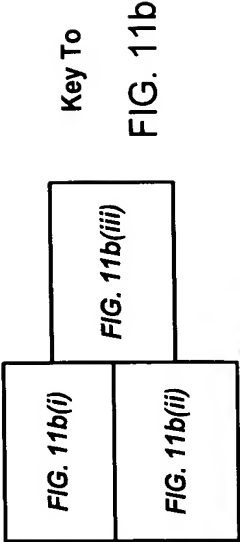


FIG. 11b

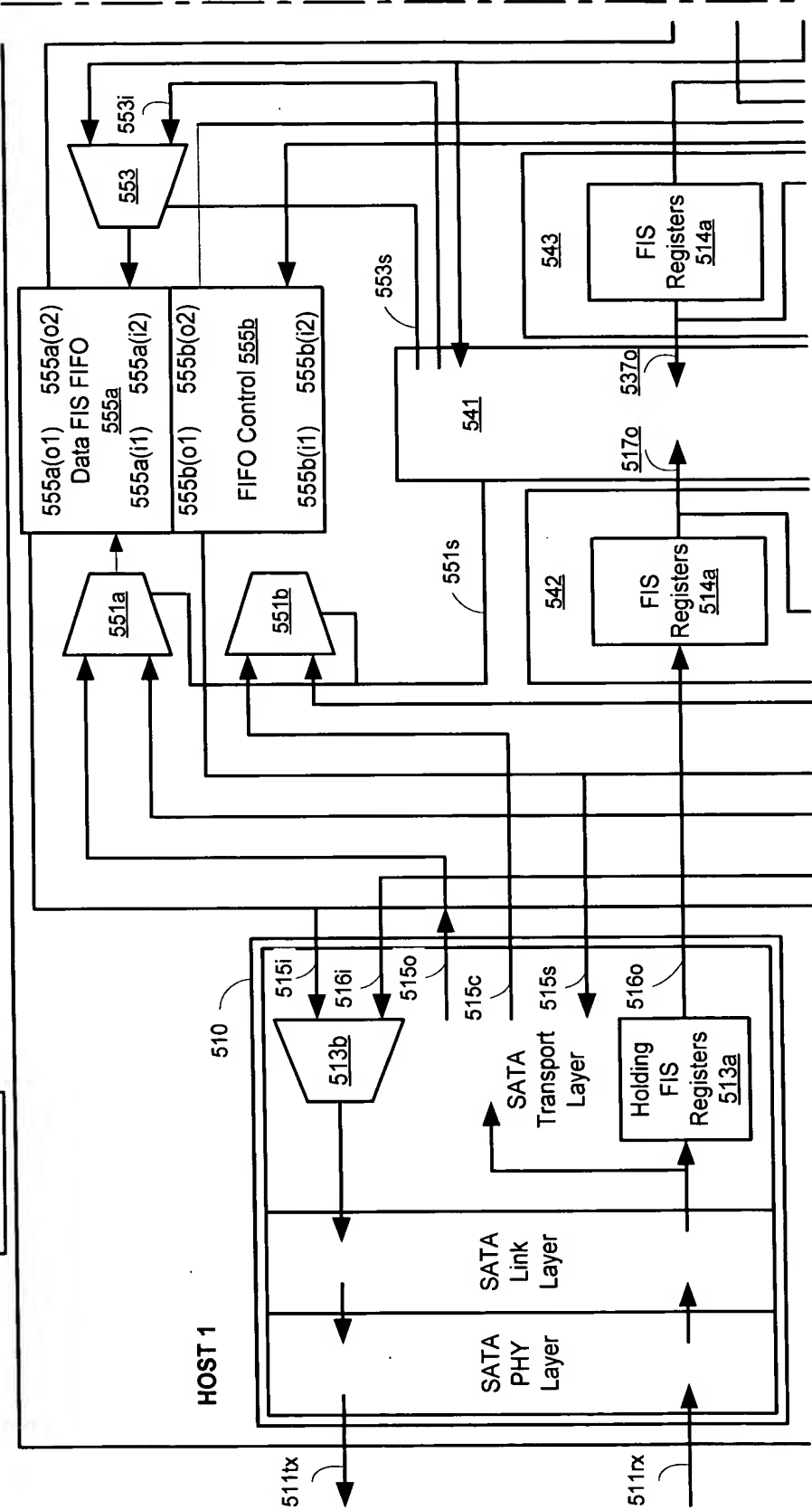


FIG. 11b(i)

37/43

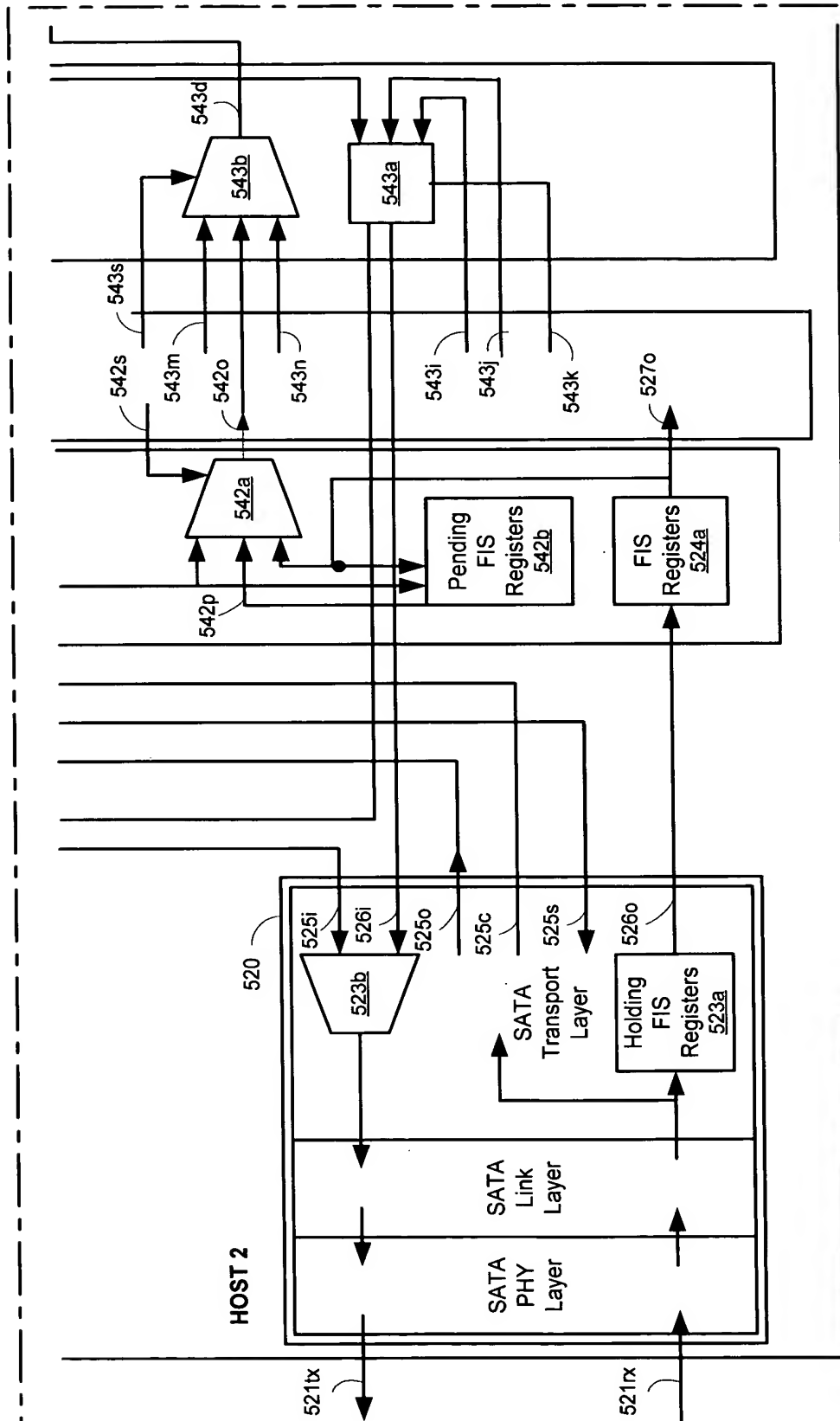


FIG. 11b(ii)

38/43

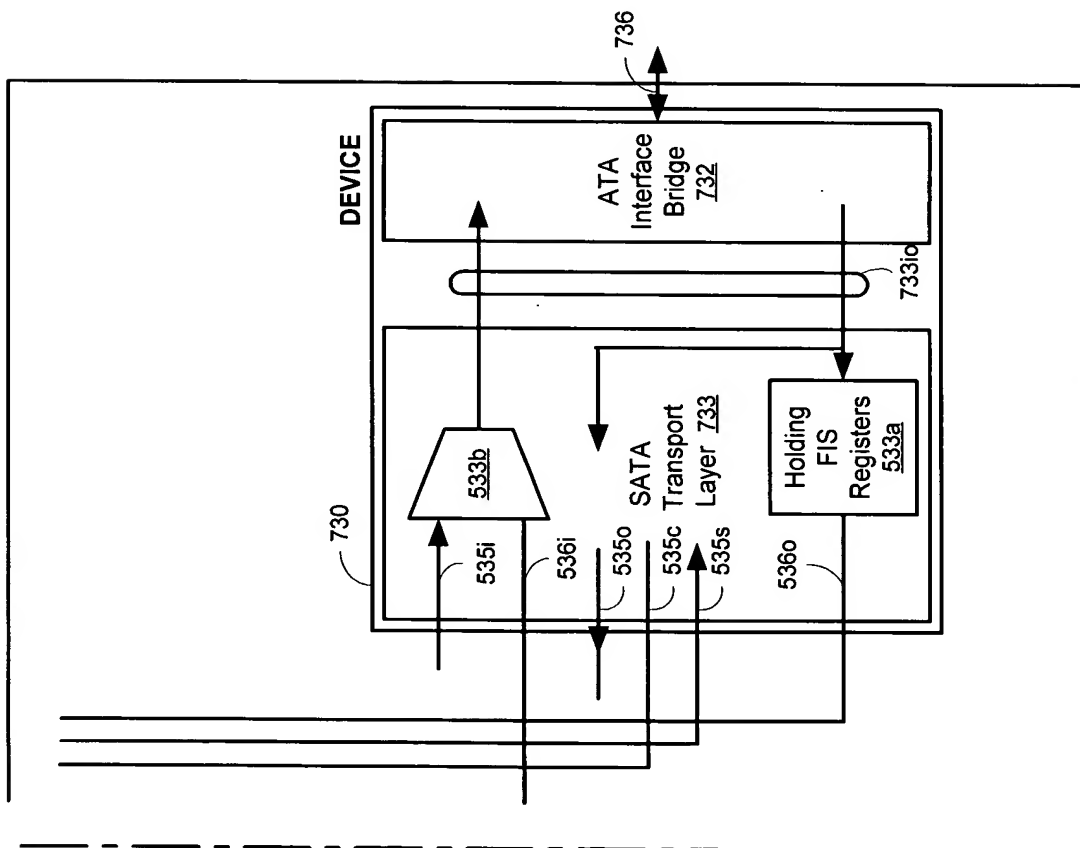


FIG. 11b(iii)

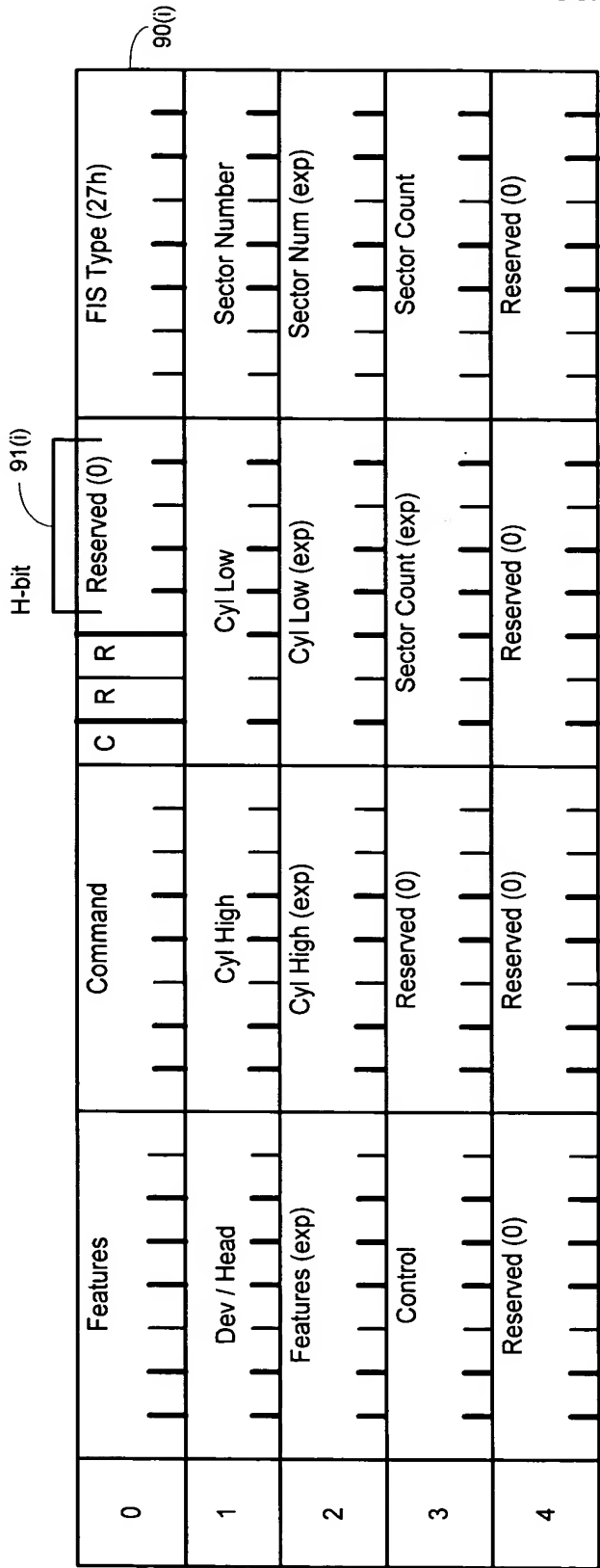


FIG. 12(i) Register FIS Host to Device

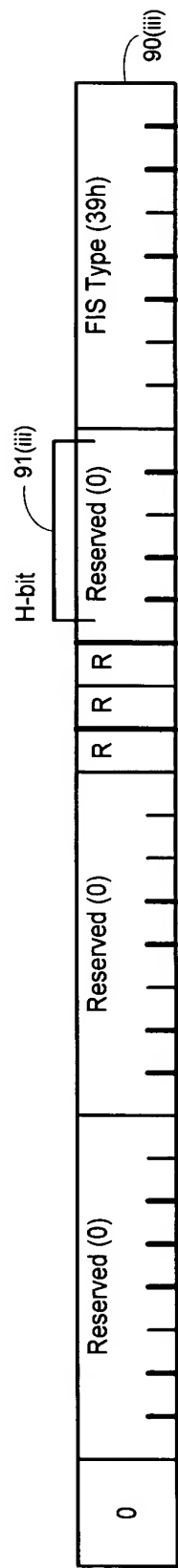


FIG. 12(iii) DMA Activate FIS, Device to Host

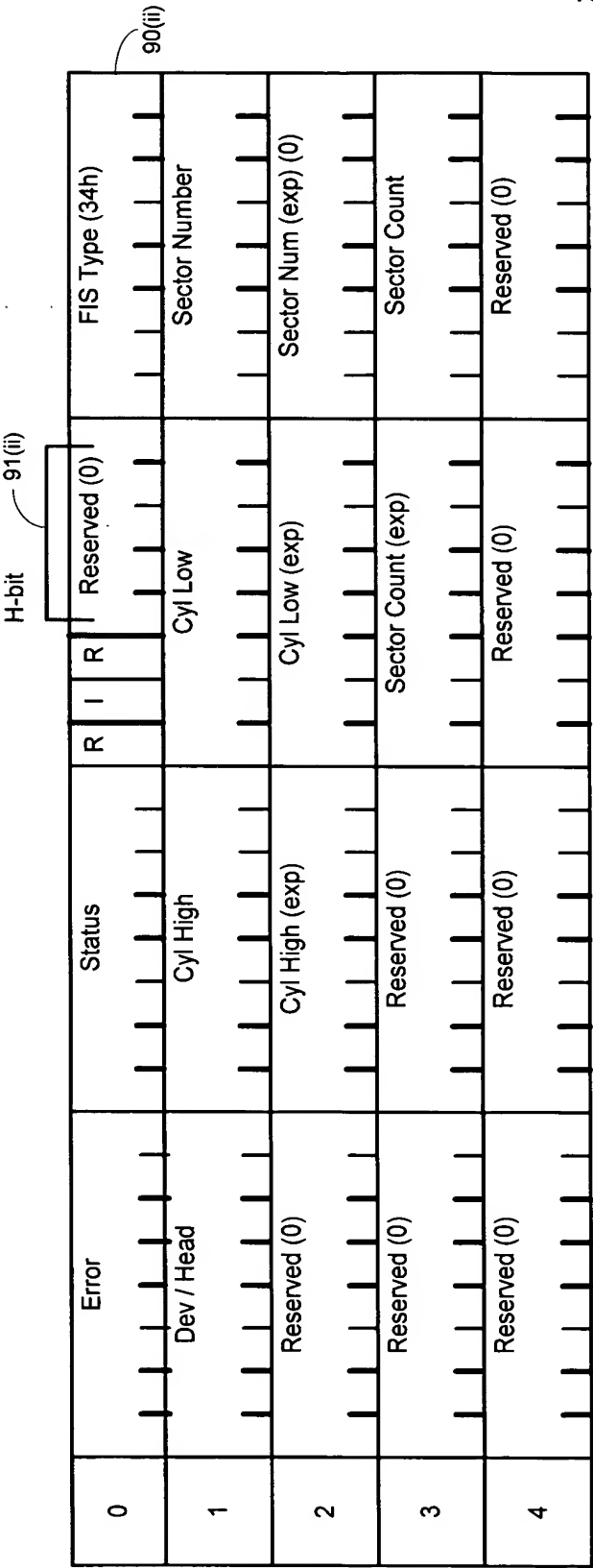


FIG. 12(ii) Register FIS Device to Host

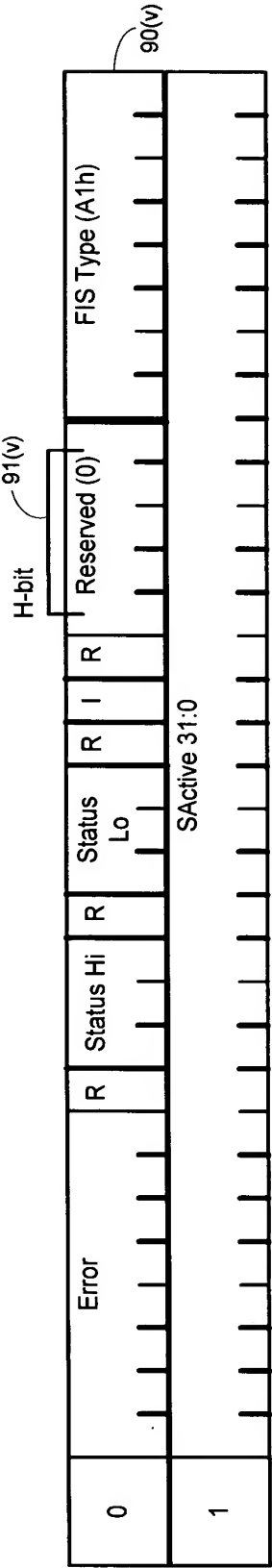


FIG. 12(v) Set Device Bits FIS



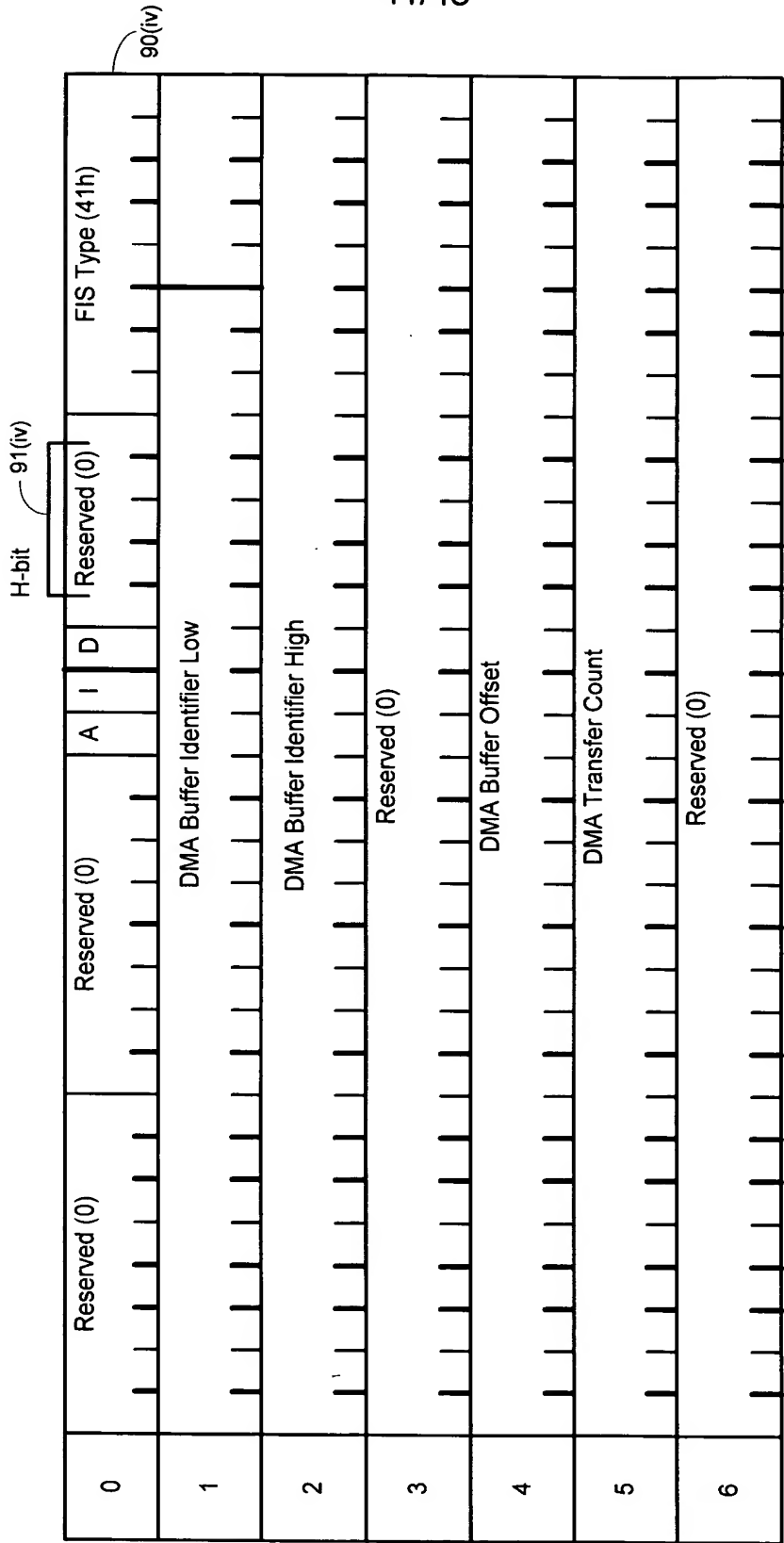


FIG. 12(iv) DMA Setup FIS

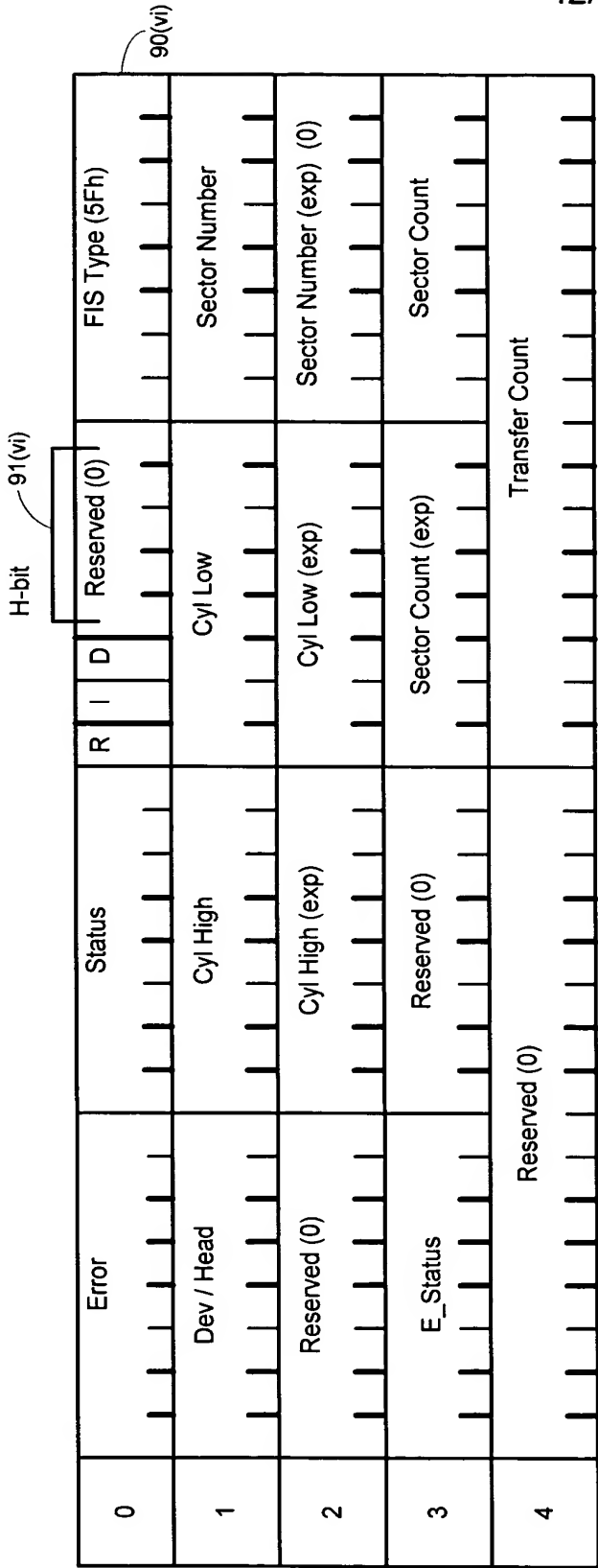


FIG. 12(vi) PIO Setup FIS

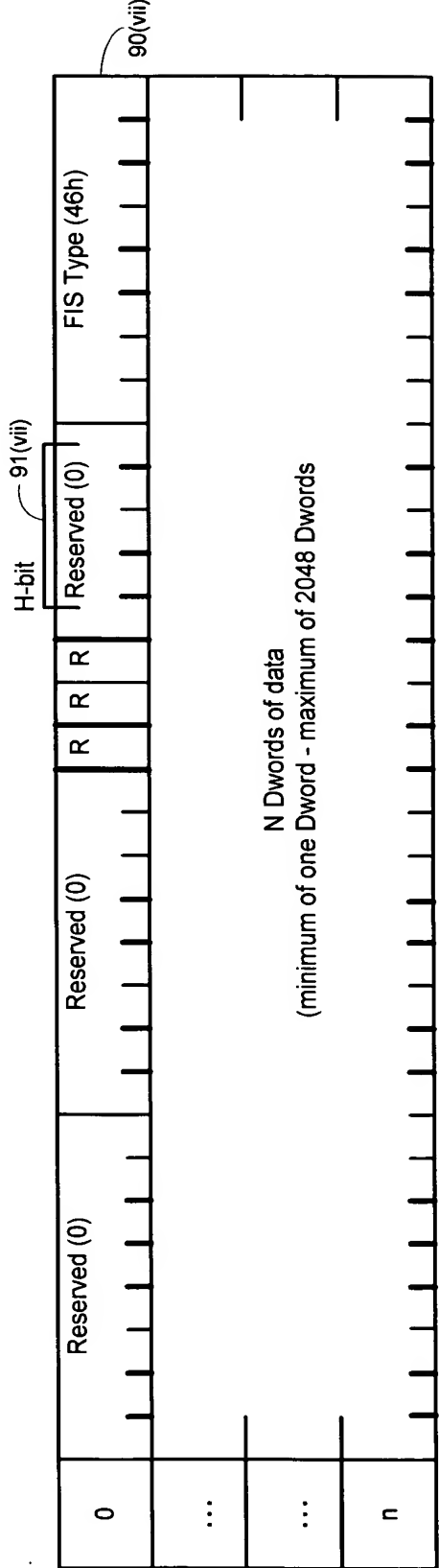


FIG. 12(vii) Data FIS

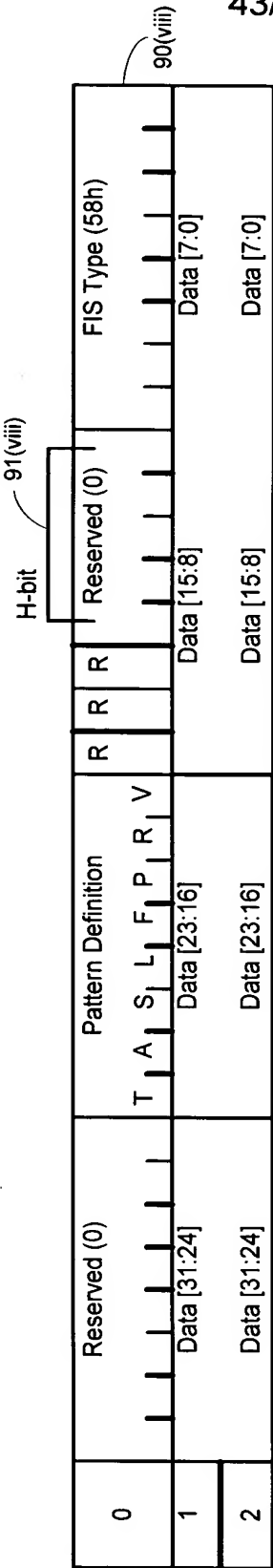


FIG. 12(viii) BIST Activate FIS